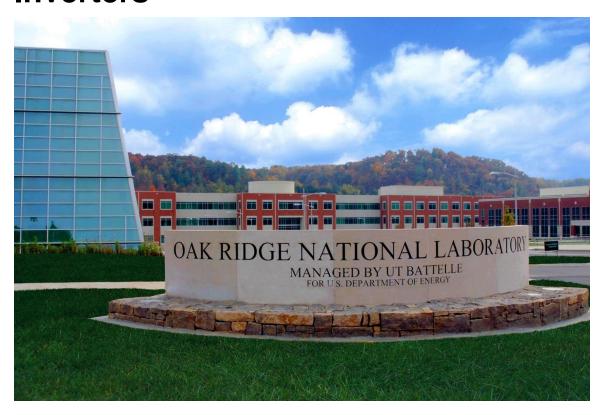
# Open Source Fault-tolerant Grid Frequency Measurement for Solar Inverters



Lingwei Zhan Bailu Xiao Fuhua Li Yilu Liu

September 2021



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Electrification and Energy Infrastructures Division

# OPEN SOURCE FAULT-TOLERANT GRID FREQUENCY MEASUREMENT FOR SOLAR INVERTERS

Lingwei Zhan Bailu Xiao Fuhua Li Yilu Liu

September 2021

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#### 2. EXECUTIVE SUMMARY

Many critical electric grid operations rely on accurate system frequency measurement [1], however, the frequency measurement can be easily undermined by frequent transient events. On August 16, 2016, a false frequency measurement of below 57 Hz during a series of system faults led to the tripping of 700 MW solar inverters instantaneously in Southern California, United States [2]. Later, it was found out that the actual system frequency only dropped to about 59.867 Hz, and the frequency measurement algorithm used in solar inverters failed to provide the accurate measurements [2]. In addition, hundreds of such events at lower magnitude have happened based on records from NERC. After this event, recommend by NERC [2], inverter manufactures and affected utilities developed a temporary correction plan by implementing a five (or two) -second time delay for low (or high) frequency protection to avoid the potential false trips caused by incorrect frequency measurements [2]. However, this approach is far from perfect because the added time delay will prevent solar inverters from taking protective actions if system frequency does deviate from nominal frequency significantly.

The event revealed that the problem is that the frequency measurement during system transient faults could have large errors, which could cause the operation of protection system mistakenly. To solve the problem, we first investigated and tested existing measurement technologies, and understand the technical challenges of grid measurements during system transients. Based on investigation and test, we developed the fault-tolerant measurement technology that will overcome the measurement problems of existing measurement methods, thus providing reliable measurements for grid controls and protections. The developed technology has been optimized and evaluated under different types of steady-state and transient conditions. Further, the developed measurement method has been implemented on low-cost hardware to verify it has low computation cost for easy system integration. The key advantage of the developed measurement technology is that it can provide reliable measurement during system transients, and it not only contributes to a highly secure protection system, but also contributes to many other grid applications that rely on reliable measurements such as frequency regulation, smooth DER integration, voltage regulation, stability predication, fast load shedding. Additionally, because the grid measurement is a fundamental component for many devices/systems (e.g., Relays, PMUs, wind turbine, energy storage, etc.), the success of this project will have a broad impact on these areas, ultimately contributing to a more reliable and secure bulk system.

Some key features and performances of the new technology have been summarized as follows.

- System transient faults, particularly phase angle step change that can cause large measurement errors, could be detected within a quarter fundamental frequency cycle.
- The maximum frequency measurement errors in two cycles following a transient fault with phase angle change are within 0.05 Hz, and the errors are within 0.005 Hz six cycles (i.e., 0.1 second) following the event.
- The transient detector has extremely low computational cost and detects system anomalies sample by sample with high resolution and high speed. The lightweight detector enables its easy implementation into hardware without introducing much additional hardware burden.

- Working with the transient detector, the adaptive frequency estimator can minimize frequency
  measurement errors during system transients, thus providing reliable and accurate measurements
  during transients.
- From the system point of view, the fault-tolerant measurements provided by the developed measurement technology can enhance the protection reliability and security during system transient events. In addition, thanks to the robust and accurate measurements, the performances of the grid services participated by DERs could be also improved.

To promote the developed technology for further development and industry adoption, the developed technology has been published in the paper "Fault-tolerant grid frequency measurement algorithm during transients", and "Recent Development of Frequency Estimation Methods for Future Smart Grid", which are all published in open access journals. The project outcomes were also presented in 2019 IEEE PES general meeting and several times in North American SynchroPhasor Initiative (NASPI) conference. A company plans to utilize our technology to develop a new grid monitor.

#### 3. BACKGROUND

The Discrete Fourier transform (DFT) based measurement algorithms are one of the most common measurement algorithms for grid parameter estimation such as rms, phase angle, frequency [3]. Over the past few years, many DFT based algorithms have been developed to enhance its measurement accuracy under steady-state and/or dynamic grid conditions [4]-[17]. For example, an adaptive band-pass filter utilizing exponential modulation filter has been proposed to reduce measurement errors at the presence of large frequency deviations [6],[7]. Measurement accuracy of different algorithms including FIR filter, extended Kalman filtering (EKF), and enhanced DFT method have been compared in detail under different grid conditions [13]. Two artificial signals that have 90-degree phase difference were constructed by the Clarke transformation to address the frequency spectrum leakage of DFT [16]. A multi-module approach was developed to enhance both steady-state and dynamic measurement accuracies [17], in which each module was developed to eliminate some specific errors. Besides DFT-based measurement algorithms, some signal model-based algorithms have been developed to further improve the accuracy under dynamic conditions [18]-[21]. However, a key drawback of the state-of-the-art algorithms is that they cannot perform measurements accurately during system transient faults. In the Blue Cut Fire event, there was a phase angle jump of about 26 degrees in the voltage waveform during the transient fault. The phase angle jump fault will cause waveform discontinuity, and these algorithms will fail to provide reliable measurements during this period because they typically assume the waveform to be measured is continuous, no matter what method (DFT, PLL, EKF, FIR, or Taylor WLS) is used for estimation.

In fact, the measurement errors during the system transient faults like phase-jump is not required in the IEEE Standard [22]. As a result, although a measurement instrument can pass the strict IEEE Standard, it could still be the source of the problem in the future if we have similar system transient faults, which could happen again. Therefore, developing the fault-tolerant measurement technology is the key to solve the problem.

#### 4. PROJECT OBJECTIVES

For existing measurement technologies, they lack the capability to "perceive" transient faults, which leads to significant measurement errors during transients. The overall objective of this project is to develop a fault-tolerant frequency measurement algorithm, as illustrated in Figure 1, thus providing a fundamental solution to frequency dependent grid control and protection. Overall, we proposed a two-step approach (a transient fault detector followed by an adaptive frequency estimator) for fault-tolerant frequency estimation. To be specific, we developed a real-time "transient detector" to detect the waveform discontinuities such as phase jumps and magnitude step change, which are responsible for significant frequency errors. With the help of successful discontinuities detection, then we developed an adaptive and intelligent frequency estimator. According to the transient detection information provided by the transient detector, the frequency estimator can adjust the estimation window to minimize the estimation errors during system transients. The complete measurement method consisting of transient detector and intelligent frequency estimator were tested and optimized by synthetic signals and transient fault signals from IEEE benchmark system models to evaluate the measurement performance in terms of accuracy, dependability, and security. Further, the developed measurement technology was implemented on lowcost hardware and the real-time measurements were achieved. It demonstrated the low computation cost of the method, which is critical for easily and widely industry adoption.

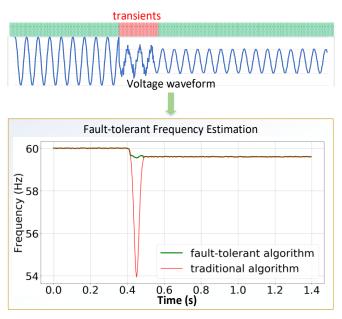


Figure 1 Illustration of fault-tolerant frequency measurement

#### 5. PROJECT RESULTS AND DISCUSSION

#### 5.1 FAULT-TOLERANT GRID MEASUREMENT ALGORITHM

# 5.1.1 Technical Approach

Existing measurement algorithms suffer from significant measurement errors since the measurement model of these algorithms assume the measured grid signals are in the form of cosine waveform as follows:

$$x(t) = \sqrt{2}A_0\cos(2\pi f t + \varphi)$$
 (1)

where  $A_0$  is the magnitude of the of power grid signal, f is the fundamental frequency of the power grid signal, and  $\varphi$  is the phase angle of the power grid signal.

For existing grid measurement algorithms, these parameters are assumed to be constant or slow-varying variables, so the measurement errors are low when the assumptions are true. Unfortunately, during power grid system transient events, these parameters can experience significant and sudden change, such as phase angle or magnitude step change, which can introduce substantial errors to measurement results. For example, during a phase angle step change, the grid signal x(t) will exhibit discontinuity feature. Figure 2 shows the point-on-wave of the grid signal with a 90-degree phase angle step change. We could clearly see the discontinuity of the signal due to the phase angle step change.

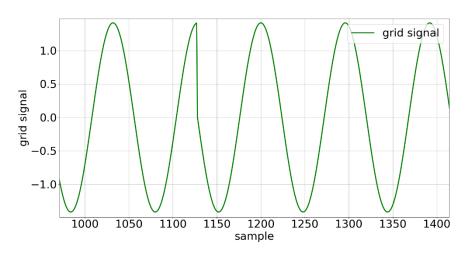


Figure 2 point on wave data with 90-degree step change

The discontinuity of signals will introduce significant measurement errors since commonly used algorithms assume the measured grid signals are slow varying steady signals. Figure 3 and Figure 4 show the frequency measurements of a DFT based and a Phase-locked loop (PLL) based measurement algorithm, respectively.

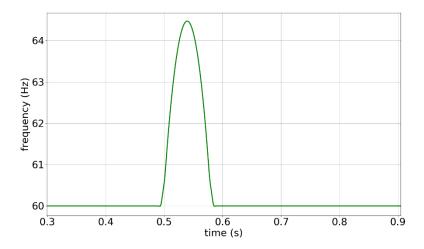


Figure 3 Frequency measurements of DFT based frequency estimation algorithm

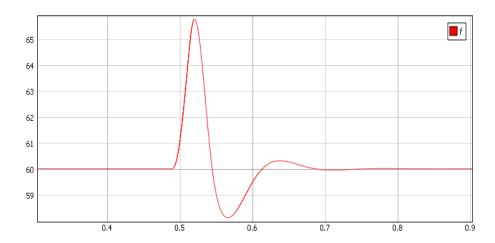


Figure 4 Frequency measurements of PLL based frequency estimation algorithm

It can be observed that when the grid signals have a step change, the measurement algorithms will have large measurement errors.

The existing measurement algorithms could not tolerate such signal distortions because they cannot "see" the distortions of the grid signals, therefore they cannot adapt to the change of grid signals. In this project, we propose a two-step approach for fault-tolerant frequency measurements. In the first step, we develop a grid transient detector, and it will be able to detect the transients of grid signals, particularly the step change of the grid signals. The second step is the adaptive frequency estimator, and it will be able to adapt to the grid transient events based on the detection results in the first step. The overall concept of this approach compared to the traditional algorithms are shown in Figure 5.

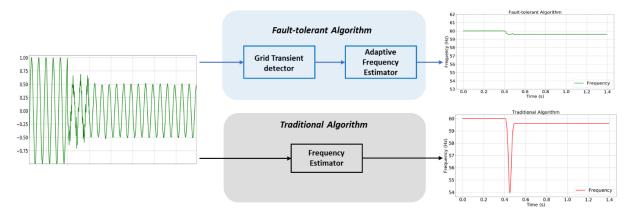


Figure 5 Architecture of the proposed fault-tolerant algorithm vs traditional algorithm

Therefore, the key and first step of the project is to develop a transient fault detector.

# 5.1.2 Development of a Transient Detector

When the power grid operates normally without any transient event, the grid signals can be approximately represented by the following equation:

$$x(t) = \sqrt{2}A_0\cos(2\pi f t + \varphi)$$
(2)

As the power grid signal is periodic, we will have the following equation,

$$x(t) - x(t - T) = 0 (3)$$

where T is the period of the grid signal.

When the grid signals experience transient events, the signals will be distorted, and the periodicity will not maintain. As a result, x(t) - x(t - T) will not be equal to 0. Instead,  $x(t) - x(t - T) = \alpha$ . The larger of the absolute value of  $\alpha$  (i.e.,  $|\alpha|$ ), the larger deviation of the grid signal from the normal periodic signal.

Based on this observation, the proposed fault detector is represented as follows:

$$\gamma(t) = \left| \int_{t=0}^{T} [x(t) - x(t-T)] dt \right| (4)$$

It is the integration of x(t) - x(t - T) over one fundamental frequency cycle of grid signals. For steady-state grid signals, x(t - T) is close to 0, therefore  $\gamma(t)$  will be close to 0.

In practice, the grid signals are digitally sampled. Assuming there are N samples in one fundamental frequency cycle. Then the detector equation could be represented as follows,

$$\gamma(k) = \left| \sum_{k=0}^{N} x(k) - x(k-N) \right| (5)$$

Figure 6 shows a simulation of the detector value  $\gamma$  (Figure 6(b)) when the measured signals are ideal sinusoidal (Figure 6(a)). It verified that ideally the detector value  $\gamma$  is equal to 0, as expected in previous analysis.

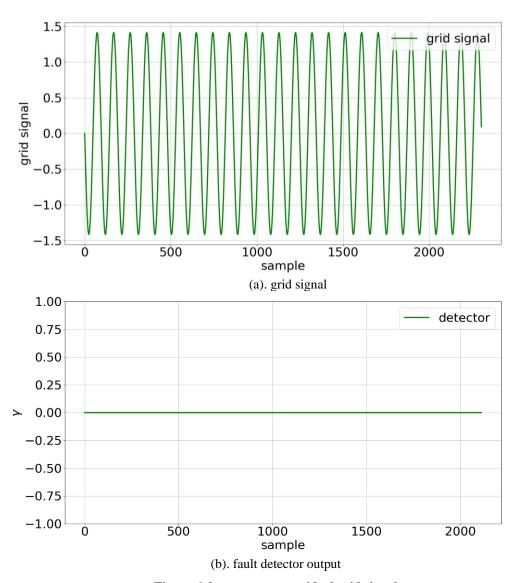


Figure 6 detector output - ideal grid signal

The detector has been implemented using Python/C++ for performance testing. Consider the grid signals are usually distorted by noise and harmonics. Therefore, the performance of the detector needs to be evaluated under these conditions.

#### **5.1.2.1** Noise Test

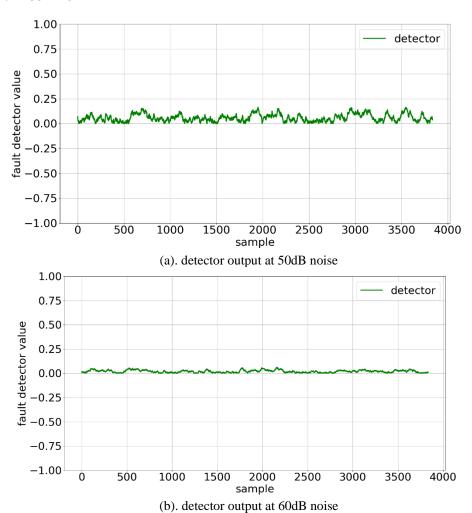
According to our previous research [17], [23], the noise level of grid signals is about 60-70 dB. Please note that although we have developed linear averaging filter to minimize the noise in previous work [17], [23], which could significantly reduce the noise. In this test, we still use high noise level for a more conservative testing. The impact of noise at different levels from 50 dB to 80 dB on detector has been evaluated and the results are shown in Figure 7 and summarized in Table I.

We can see that the impact of noise on the detector is weak.

Table I Maximum fault detector output under different levels of white noise

Noise level (dB)	50 dB	60 dB	70 dB	80 dB
Fault detector output (γ)	0.25	0.1	0.025	0.0125

The robustness of the detector to the noise is crucial for reliability of the fault detector because we need avoid falsely triggering the fault detector due to the noise.



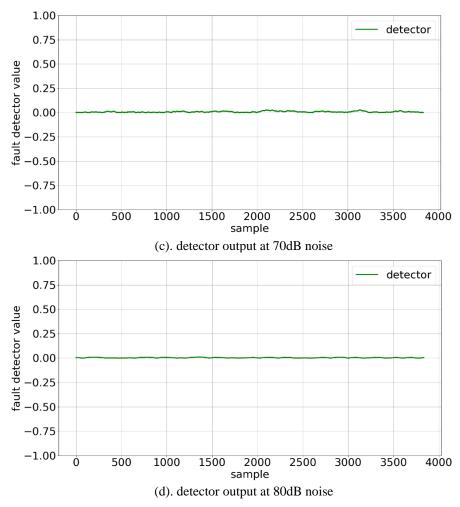


Figure 7 detector output –grid signal with different levels of noise

#### 5.1.2.2 Harmonics test

The power grid signals could also be distorted by harmonics. 10% of harmonics is used for testing, which is much higher than the typical harmonic magnitude in actual environments. It should be also noted that in practice harmonics will be filtered by low-pass filter (analog or digital) to minimize its impact on grid measurements. The higher of the order of the harmonics, the lower its magnitude thanks to the low-pass filter. The raw point-on-wave grid waveforms with harmonics are also shown in the following figures (Figure 8 - Figure 11) for better understanding.

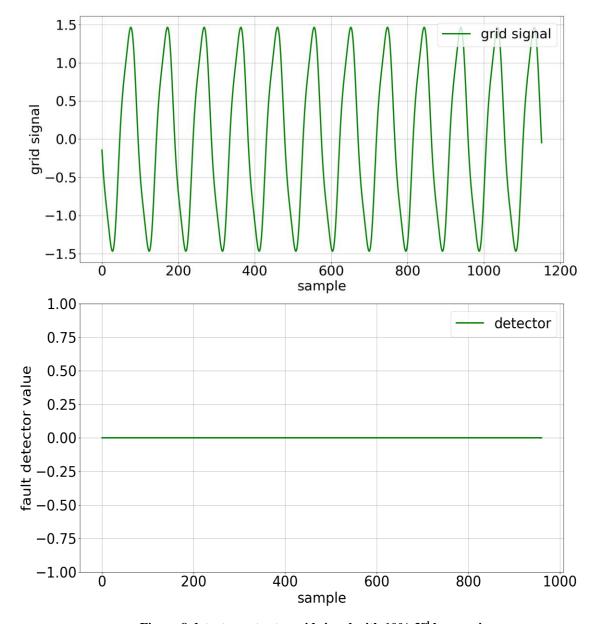
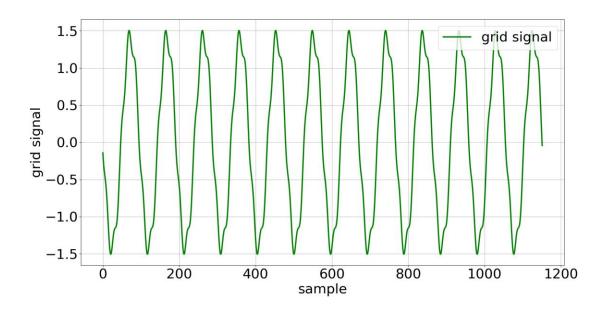


Figure 8 detector output – grid signal with 10% 3<sup>rd</sup> harmonic

It could be observed that although the grid signals are highly distorted by harmonics, the impact of harmonics on fault detector is low. This is also very important since usually frequency estimation algorithms can tolerate harmonics, so it is desired that the signal distortions caused by harmonics do not impact the fault detector.



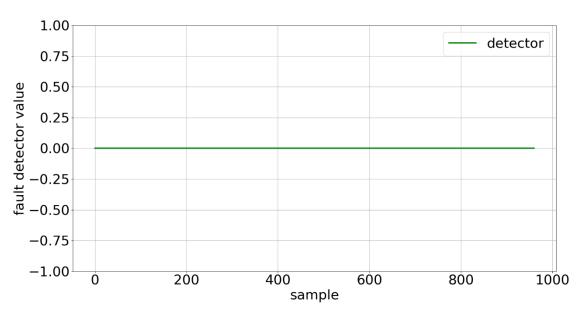
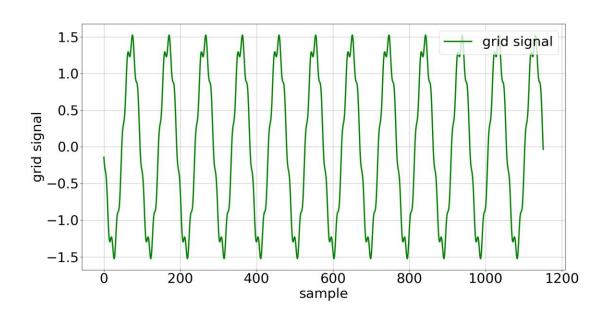


Figure 9 detector output - grid signal with 10% 5th harmonic



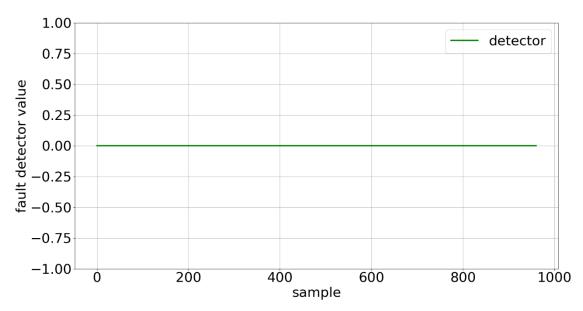
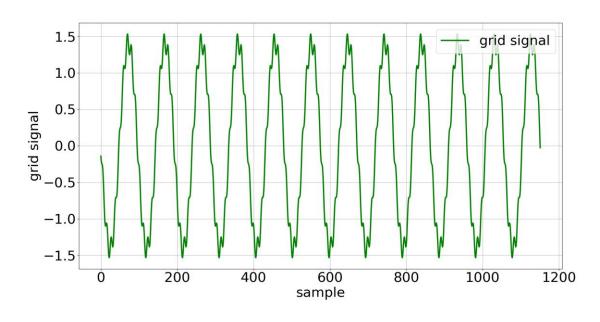


Figure 10 detector output – grid signal with 10% 7th harmonic



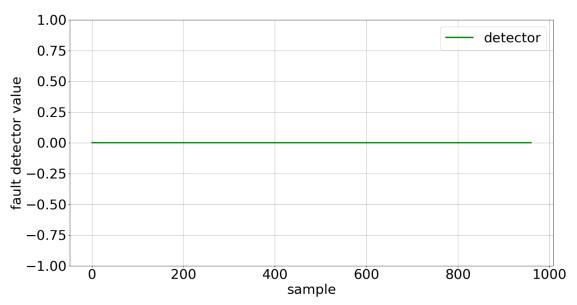


Figure 11 detector output – grid signal with 10% 9th harmonic

# 5.1.2.3 Phase angle step change test

In the Aug. 2016 large scale solar tripping event, significant frequency measurements errors are caused by the phase angle step change during a system fault. As discussed earlier, the phase angle step change will introduce a break point to the original continuous and smooth waveform, and this break point will introduce significant measurement errors. Therefore, successful detection of the occurrence of the break point is critical to minimize the measurement errors, ultimately contributing to fault-tolerant measurements.

To perform the testing, a total number of 143 tests have been done. The phase angle step change in each test is as follows:

$$\theta = -360 + 5 * i$$
,  $i = 1, 2, ..., 143$  (6)

Figure 12 shows the value of detector with respect to the phase angle step change from -360 degree to 360 degree at a step change of 5 degrees, as given in (6).

Figure 13 shows the frequency measurement errors with respect to the phase angle step change. From the two figures, we can find a proportional relationship between fault detector output value and frequency measurement errors. This will provide us the foundation to limit the frequency measurement errors by tuning the threshold of the fault detector.

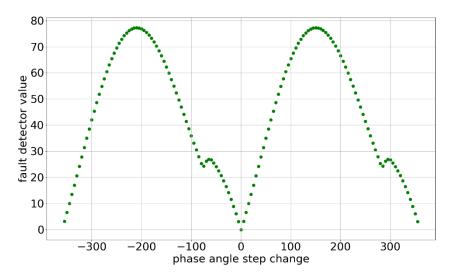


Figure 12 detector output – grid signal with different phase angle step change

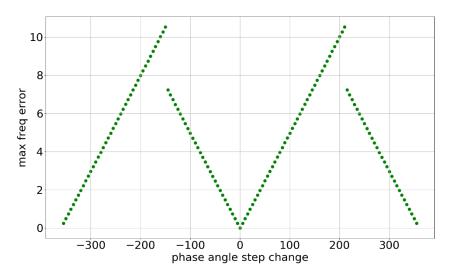


Figure 13 Frequency – grid signal with phase angle step change

## 5.1.3 Development of an adaptive frequency estimation algorithm

In last section, we develop a grid transient detector. It has been successfully tested, and it can detect the transients of grid signals, particularly the step change of the grid signals. The second step is the frequency estimator, and it will be able to adapt to the grid transient events based on the detection results in the first step. A high-level flowchart of the second step is shown in Figure 14. The detection results from grid transient detector will be compared to a transient threshold. If the result exceeds the threshold, which means a system transient is detected, the output of adaptive frequency estimator will be locked to the latest frequency measurements. When the outputs of the grid transient detector are lower than the threshold for one fundamental frequency cycle (1/60 second for 60 Hz system), the extra fast frequency estimator, using a short estimation window of 2 cycles, will start to estimate the grid frequency. If the outputs of the transient detector fall below the threshold for 2.25 cycles, the fast frequency estimator, using an estimation window of 3 cycles will start to estimate the grid frequency. When the output of the transient detector is below the threshold equal or greater than 4.75 cycles, the classical frequency estimation algorithm with an estimation window size of 5.75 cycles will start to provide frequency measurements.

First, the threshold used for determining the occurrence of system transients needs to be determined. As discussed in last section, we did phase angle step change test, and the phase angle step change in each test is as follows:

$$\theta = -360 + 5 * i$$
,  $i = 1, 2, ..., 143 (7)$ 

Additionally, as the step change can occur at any time in one cycle, so the phase angle step change has been tested at different phase angles from 0 degree to 360 degree.

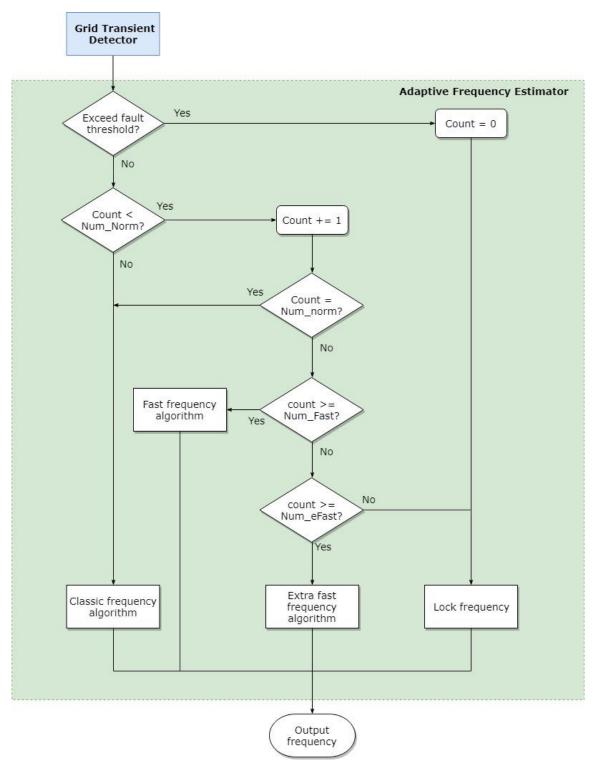
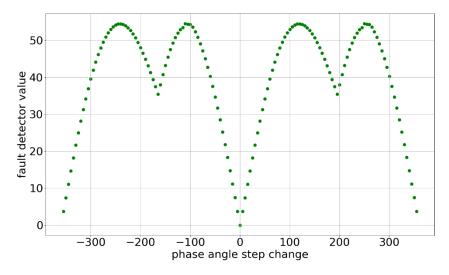
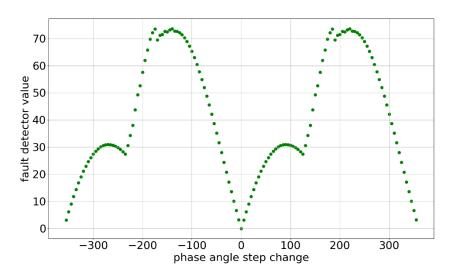


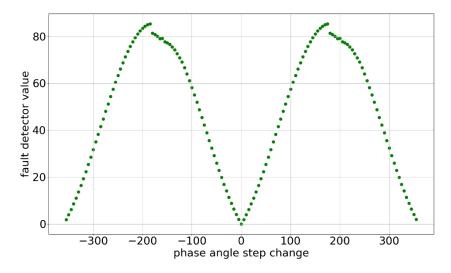
Figure 14 Architecture of the proposed Adaptive Frequency Estimator



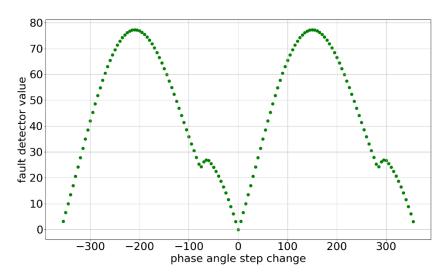
(a) Phase angle 0 degree at the time of event



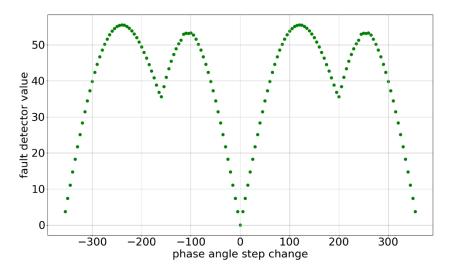
(b) Phase angle 45 degree at the time of event



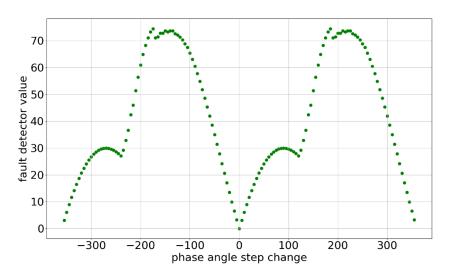
(c) Phase angle 90 degree at the time of event



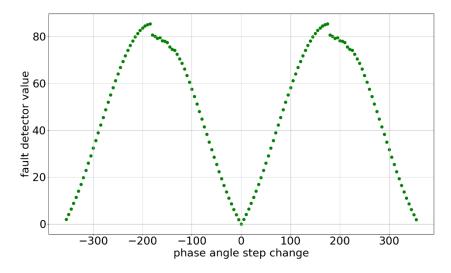
(d) Phase angle 135 degree at the time of event



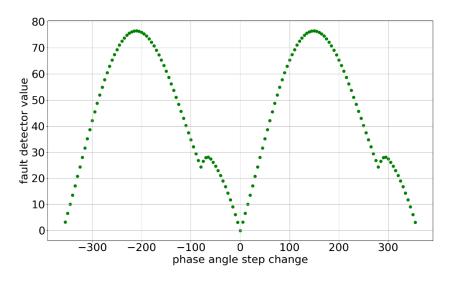
(e) Phase angle 180 degree at the time of event



(f) Phase angle 225 degree at the time of event



(g) Phase angle 270 degree at the time of event



(h) Phase angle 315 degree at the time of event

Figure 15 detector output – grid signal with different phase angle step change

From those figures, it can be seen there is a proportional relationship between fault detector output value and the absolute value of the phase angle step change when the change within a certain range. On the other hand, there is also a proportional relationship between the absolute value of the phase angle step change and frequency measurement errors. Therefore, the threshold can be determined by checking the detector values of different phase angles at the time of event with the phase angle step change in a small range. Here phase angle step change from -20 degree to 20 degree is chosen. Figure 16 shows the fault detector value with respect to the different phase angles at the time of event. The fault threshold should be determined by the lowest value. In each test, we monitor whether the frequency deviation is over 0.5 Hz. If that happens, we will find the maximum value of detector's output until when the frequency deviates to 0.5 Hz and save this value for next step. Next, as we run from -20 degree to 20 degree, we will find the minimum value from a set of the maximum values, and we plot this minimum value as a "dot" in Figure

16. At last, we choose the minimum value from all the "dots" and make it as our threshold value. By doing this, the transient detector will make sure can detect all the phase angle step change event that cause over 0.5 Hz frequency errors.

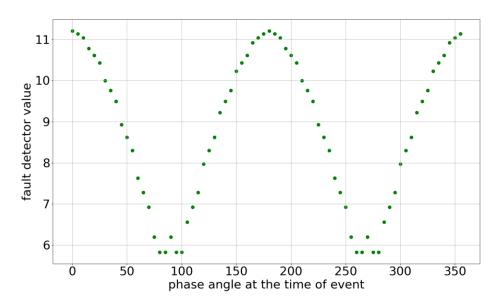


Figure 16 detector output –grid signal with different phase angle at the time of event (sampling frequency: 5760 Hz, signal frequency: 60 Hz)

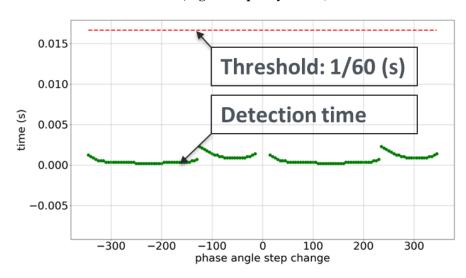


Figure 17 Detection time at different phase angle step change

## Milestone #1 tests

Figure 17 shows the test results for Milestone #1. The total number of tests is 143. Out of the total 143 tests, there are 134 tests that cause over 0.5 Hz frequency errors. The transient detector successfully detected all the tests, and Figure 17 shows the detection time for each test. We can see the detection time of all tests are below 1/60 second.

Milestone	Metric Definition	Finished Date	Success Value
1	Occurrence of system transient events that could cause over 0.5 Hz frequency measurement error is detected within one fundamental frequency cycle (1/60 second) at a success rate of above 95% by measuring the time from transient occurrence to successful detection.	1/31/2019	100%

As we discussed in last section, the detector equation could be represented as follows,

$$\gamma(k) = \left| \sum_{k=0}^{N} x(k) - x(k-N) \right| (8)$$

where N is the number of samples in one fundamental frequency cycle. Thus, the sampling frequency will be a dominant factor for the fault threshold value. And for a fixed sampling rate, the signal frequency and the signal magnitude will impact the fault threshold.

First, the relationship between the signal frequency and the fault threshold is studied. Figure 18 shows the fault threshold with respect to different signal frequency at a sampling rate of 5760 Hz. The magnitude of signals tested here is 1 per unit. As shown in the figure, the fault threshold value decreases as the signal frequency increases and vice versa. As a result, for a fixed sampling rate, signal with higher frequency should be used to determine the threshold. Here signal of frequency 63 Hz is applied to determine the fault threshold.

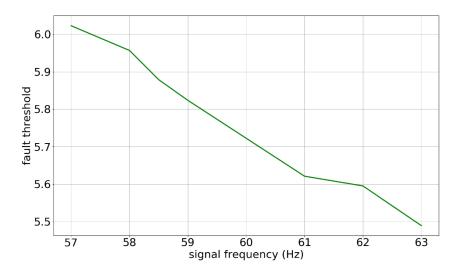


Figure 18 fault threshold value with different signal frequency (sampling rate: 5760 Hz)

Next, the relationship between the signal magnitude and the fault threshold is studied. The signals with different magnitude are tested. Figure 19 presents the fault threshold with respect to different signal magnitude at the same sampling frequency and signal frequency.

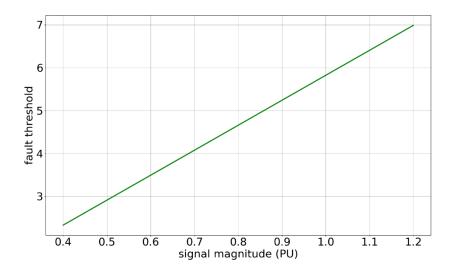


Figure 19 fault threshold value with different signal magnitude (sampling frequency: 5760 Hz, signal frequency: 60 Hz)

The fault threshold is proportional to the per unit value of signal magnitude. Thus, for a specific sampling frequency, a base threshold  $THR_B$  is first determined by using signal of frequency 63 Hz with 1 per unit magnitude. Then, the threshold THR is updated by the per unit value of the tested signal magnitude

$$THR = THR_B \times M_{PU}$$
 (9)

As mentioned above, the sampling frequency will be an important factor to the fault threshold value. The base threshold value with respect to the different sampling frequency is shown in Figure 20. The threshold value is nearly proportional to the sampling frequency.

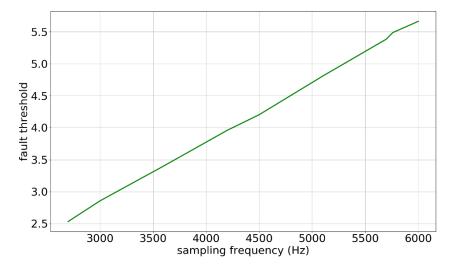


Figure 20 fault threshold value with different sampling frequency

#### Milestone 2

The fault-tolerant frequency estimator has been implemented using Python/C++ in Visual Studio 2017 for performance testing. Besides phase jumps, the test signals will be also distorted by noise, harmonics and voltage sag for a more practical evaluation. Selected test cases are listed in Table II, and results are presented below. The sampling frequency of all the test signals is 5760 Hz. According to Figure 18 and considering some margin, 5 is set as the base fault threshold.

**Table II Performance Test Cases for Fault-tolerant Frequency Estimator** 

Case	Signal	Noise	THD	Voltage	Phase angle	Max $\Delta f$ in 2 cycles (Hz)
	frequency (Hz)	(dB)		drop (%)	step change (degree)	
1	60	70	10%	50%	180	0.0005
2	60	70	10%	50%	45	0.0005
3	60	70	10%	50%	90	0.001
4	60	70	10%	50%	225	0.0005
5	60	70	10%	50%	270	0.0011
6	60	50	10%	50%	180	0.0025
7	60	70	20%	50%	180	0.0007
8	60	70	10%	90%	180	0.005
9	60	50	20%	90%	180	0.013
10	59	70	10%	50%	180	0.019
11	59	50	20%	90%	180	0.037
12	59.5	70	10%	50%	180	0.008
13	59.5	50	20%	90%	180	0.014
14	60.5	70	10%	50%	180	0.008
15	60.5	50	20%	90%	180	0.013
16	61	70	10%	50%	180	0.018
17	61	50	20%	90%	180	0.032

First, signals of frequency 60 Hz with 70 dB noise, 10% harmonics (including 3<sup>rd</sup>, 5<sup>th</sup>, 7<sup>th</sup>, 11<sup>th</sup>, and 13<sup>th</sup> order harmonics), and 50% voltage magnitude step down with different phase jumps are considered for testing, the results are shown in Figure 21 to Figure 26. Take case 1 as an example, at the time of phase jump event, the phase angle step change is 180 degree, and voltage drops to 50%. 10% harmonics is added and voltage magnitude is ramping up when the event starts. This case is also considered as a base case. Figure 21 presents the grid signal and frequency results by the classical frequency estimator and the proposed fault-tolerant frequency estimator. The zoomed-in estimated frequency by the fault-tolerant algorithm is shown in Figure 22, and the maximum frequency measurement errors is only 0.0005 Hz in 2 cycles following the phase-jump event, which is far below 0.05 Hz as we proposed in Milestone #2. To make the test more complete, the signals with 45 degree, 90 degree, 225 degree and 270 degree phase jumps are also tested, and results are shown in Figure 23 - Figure 26.

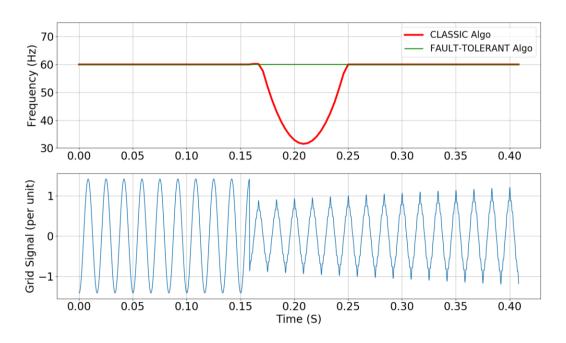


Figure 21 testing signal and results of case 1: 180-degree phase jump

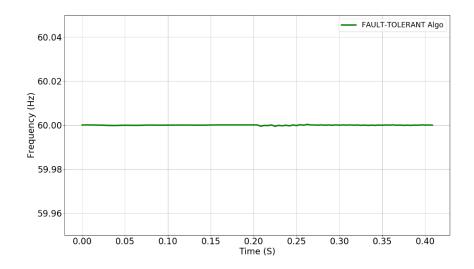


Figure 22 zoomed in estimated frequency by the fault-tolerant algorithm of case 1

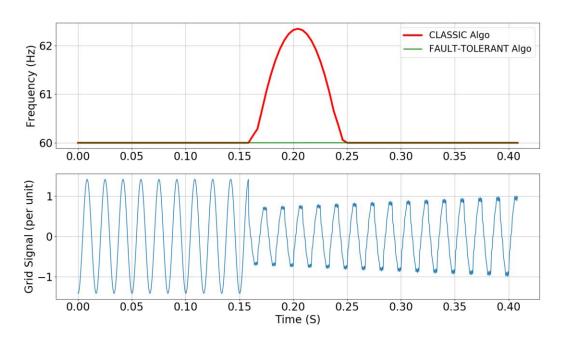


Figure 23 testing signal and results of case 2: 45-degree phase jump

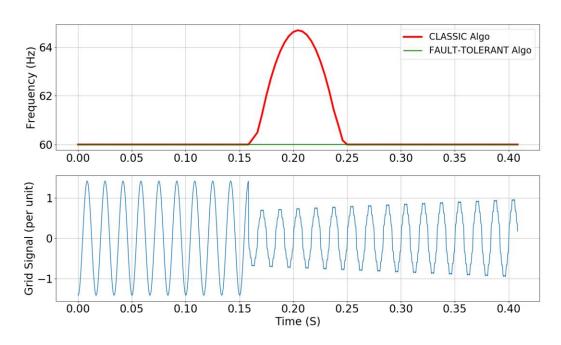


Figure 24 testing signal and results of case 3: 90-degree phase jump

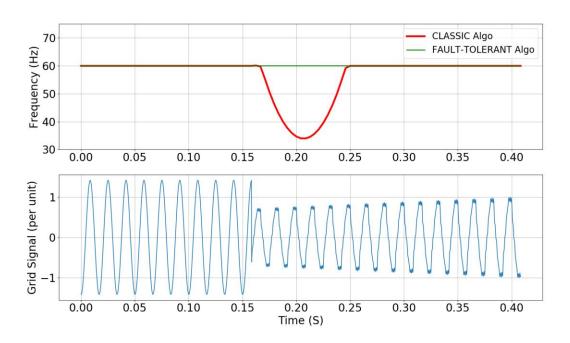


Figure 25 testing signal and results of case 4: 225-degree phase jump

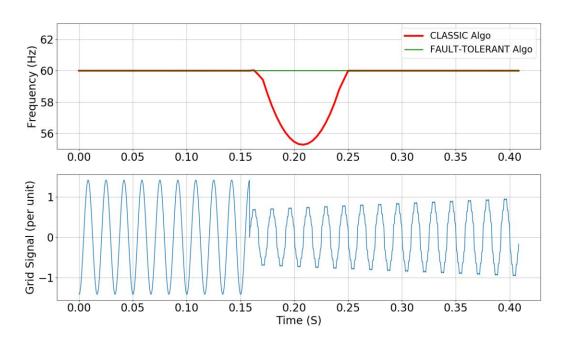


Figure 26 testing signal and results of case 5: 270-degree phase jump

Based on case 1, we have also changed the noise level, harmonics level and/or voltage drop level to make the test cases even worse and see to evaluate the performance of the fault-tolerant frequency estimator. The details of each test are listed in Table II, and results are shown below. Among those test cases, case 9 is the worst one, with 50 dB noise, 20% harmonics and 90% voltage drop at the time of phase jump, and

it can be seen from Figure 30 that the fault-tolerant frequency estimator still works well, meeting our milestone 2.

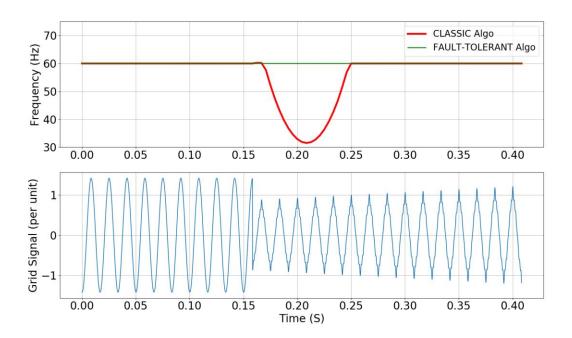


Figure 27 testing signal and results of case 6

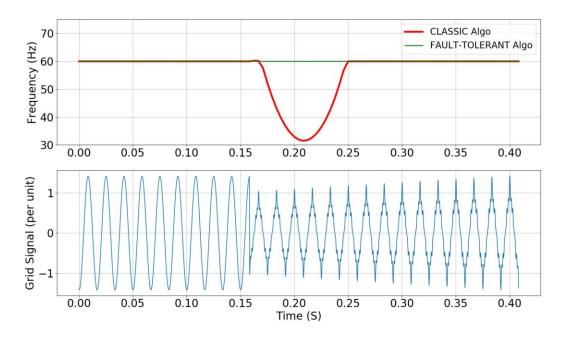


Figure 28 testing signal and results of case 7

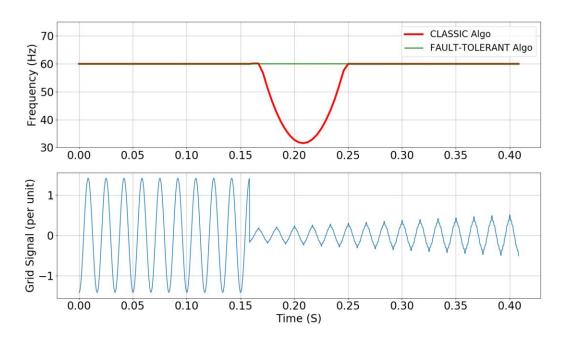


Figure 29 testing signal and results of case 8

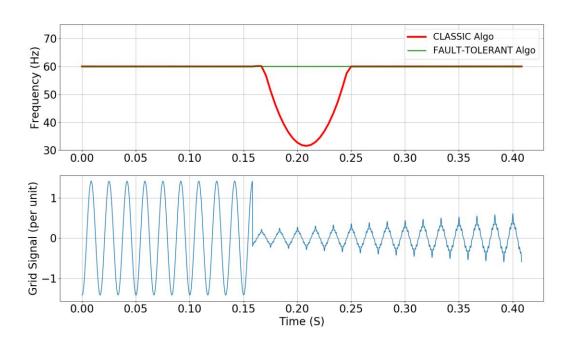


Figure 30 testing signal and results of case 9

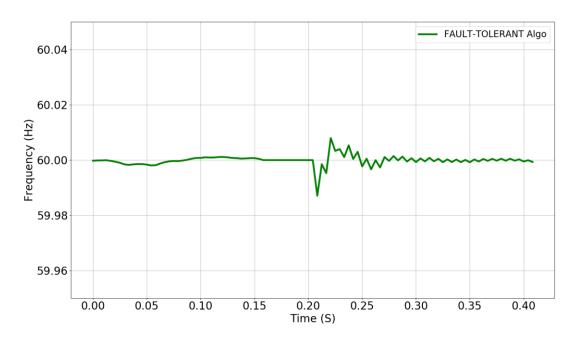


Figure 31 zoomed in estimated frequency by the fault-tolerant algorithm of case 9

We have also tested signals of different frequencies as listed in Table II, and results are presented below. The frequency measurement errors are within 0.5 Hz in two cycles and 0.05 Hz in 6 cycles following a phase-jump event, and all meet milestone 2.

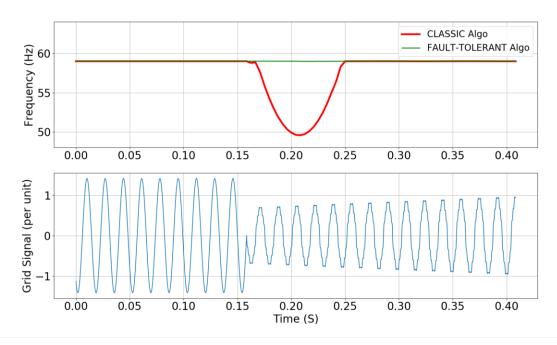


Figure 32 testing signal and results of case 10: 59 Hz, base case

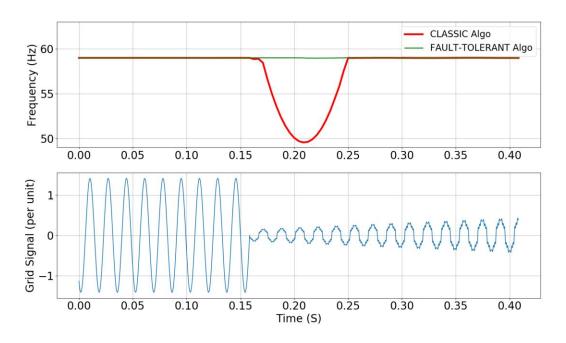


Figure 33 testing signal and results of case 11: 59 Hz, worst case

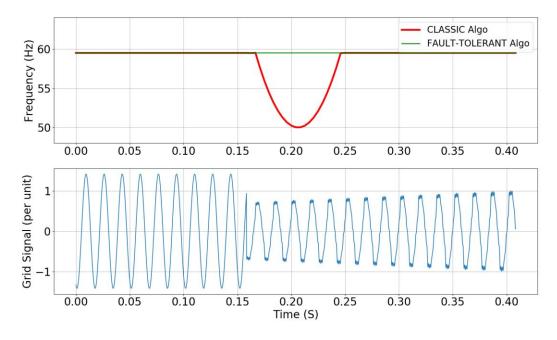


Figure 34 testing signal and results of case 12: 59.5 Hz, base case

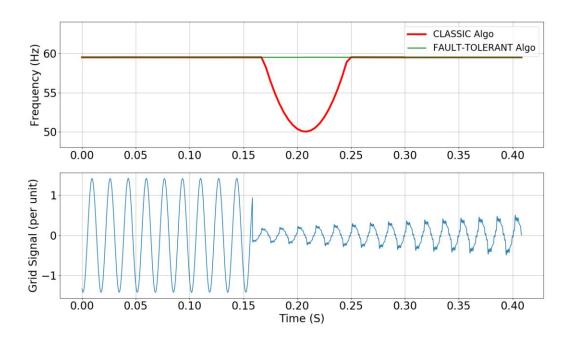


Figure 35 testing signal and results of case 13: 59.5 Hz, worst case

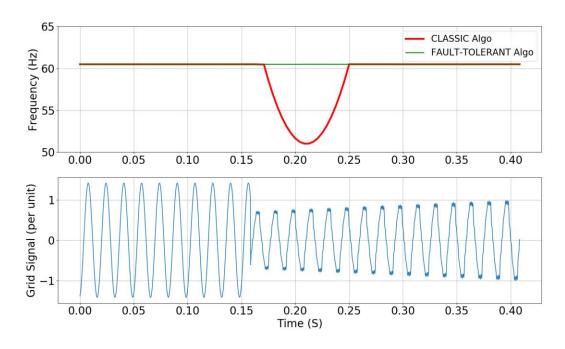


Figure 36 testing signal and results of case 14: 60.5 Hz, base case

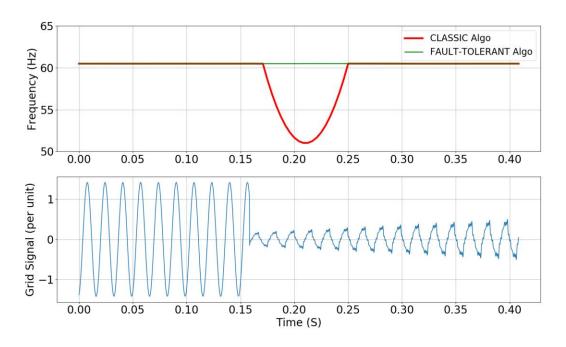


Figure 37 testing signal and results of case 15: 60.5 Hz, worst case

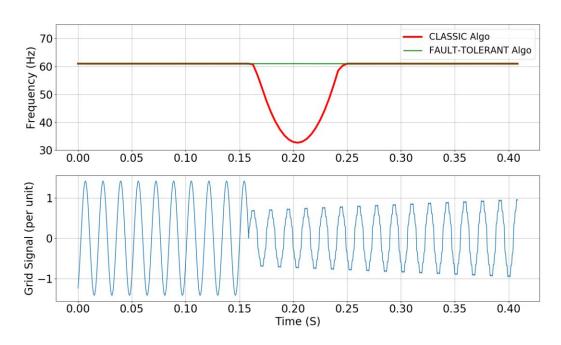


Figure 38 testing signal and results of case 16: 61 Hz, base case

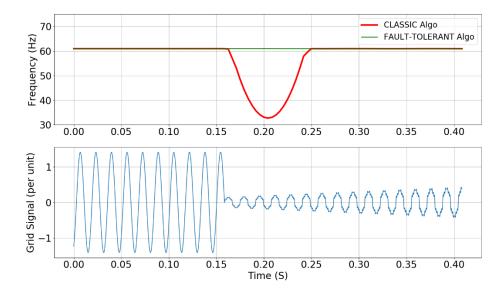


Figure 39 testing signal and results of case 17: 61 Hz, worst case

# 5.1.4 Accuracy and reliability evaluation, and further optimization of the developed algorithm

In section 5.1.2 and 5.1.3, the fault-tolerant frequency estimator has been developed Preliminary testing has been performed to evaluate its performance. In this section, the team has performed a comprehensive testing. To be specific, as a fault-tolerant algorithm, it is critical that the algorithm has both high performance in the aspect of dependability and security. Dependability means that when a transient fault occurs and the fault can cause large measurement errors, the fault-tolerant algorithm shall be able to detect it and minimize the measurement errors. Security means that the algorithm shall not response to the signal distortions that will not introduce large measurement errors. The test performed in this section is focused on both dependability and security.

#### 5.1.4.1 Dependability test of fault-tolerant algorithm

As mentioned, the dependability means the algorithm need to act when it shall act. In other words, the algorithm needs to be sensitive enough to detect a transient event and perform corresponding actions to mitigate measurement errors. In this test, the fault-tolerant frequency measurement algorithm has been tested with the fault waveforms with different levels of phase angle jump (i.e., from -60 degree to 60 degree). The phase jump waveforms are generated by applying phase-to-phase fault on the IEEE 30-Bus System and 118-Bus System, and the resultant waveforms contain not only phase jump but also voltage sag.

#### IEEE system model and system setup

Study has been performed on phase angle jump characterization associated with a fault, which has shown it mainly depends on transmission line impedance as well as the load [24]. By changing the length of transmission line, and hence the impedance, different degree of phase angle jump can be produced with a phase-to-phase fault. Theoretical analysis on a phase-to-phase fault is conducted first. Three-phase voltage can be expressed as:

$$\begin{cases} V_a = A * Sin(2\pi ft) \\ V_b = A * Sin(2\pi ft + 120^{\circ}) \\ V_c = A * Sin(2\pi ft - 120^{\circ}) \end{cases}$$

When a phase-to-phase fault happens to Phase A and B, assuming an ideal situation, the voltage near the fault location could be described as:

$$V_{ab} = A * Sin(2\pi ft) + A * Sin(2\pi ft + 120^{\circ})$$

$$V_{ab} = A * Sin(2\pi ft) + A * Sin(2\pi ft) \cos(120^{\circ}) + A * \cos(2\pi ft) \sin(120^{\circ})$$

Subsequently, this can be further simplified as:

$$V_{ab} = A * Sin(2\pi f t + 60^{\circ})$$

 $V_{ab} = A * Sin(2\pi ft + 60^{\circ})$ It can be seen from above equations; the resultant phase angle jump in a phase-to-phase fault can reach 60° in an ideal situation. Phase-A would lag 60°, whereas Phase-B would lead 60°. The resulted phase angle has a difference of 180° from Phase-C. Considering the voltage drop due to the large current, the amplitude A would have changed depending on the line impedance and the load.

To verify above equation, simulations have been conducted on the IEEE 118-bus system with PSCAD transient simulation software. Figure 40 partially displays the IEEE 118-bus system with a fault triggering module placed on Bus 60.

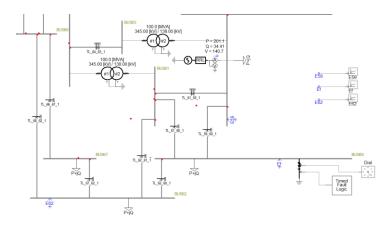


Figure 40 IEEE 118-bus system (partly shown) with line fault on Bus 60

Figure 41 shows the waveform when a phase-to-phase fault is triggered on Bus 60, and it can be observed that when the fault is triggered, the waveforms of Phase-A and Phase-B are almost overlapped with each other. By comparing with the reference waveforms without any fault, Phase-A lags 60°, and Phase-B leads 60°. The fault waveforms have 180° phase difference from Phase-C and the amplitude is reduced by 50 percent. Thus, this result can be considered as a verification of the deduced equations.

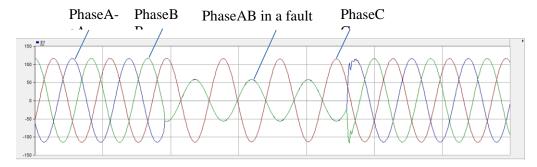


Figure 41 Voltage waveform of a phase-to-phase fault triggered at Bus 60

A series of waveforms with different phase jump angles have been generated with IEEE system model, from -60 degree to 60 degree at a step of 10 degree. Figure 42 displays the waveforms with lagging (negative) angle jumps ranging from at -10, -30, -50 degree. The fault occurs at the peak of grid signal and lasts for 0.05s. Similarly, Figure 43 displays the waveforms with leading (positive) angle jumps ranging from at 10, 30, 50 degree.

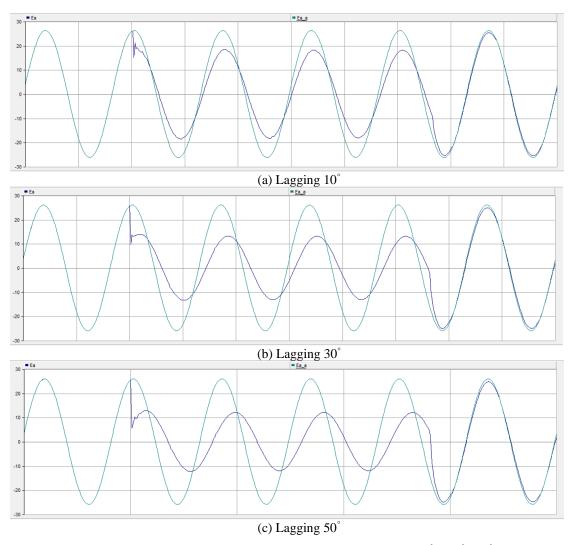


Figure 42 Voltage waveforms with lagging angle jumps at -10°, -30°, -50°

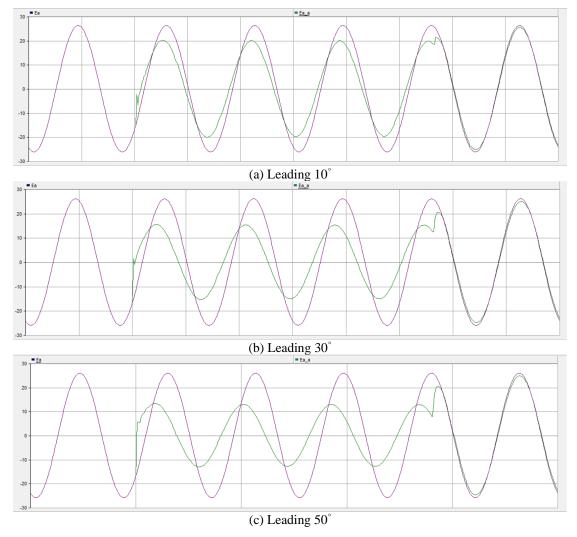


Figure 43 Voltage waveforms with leading angle jumps at  $10^{\circ}$ ,  $30^{\circ}$ ,  $50^{\circ}$ 

For the fault-tolerant frequency measurement algorithm testing, these testing waveforms have been generated with angle jumps ranging from -60° to +60° in a step of 10° (case 1-12). Additionally, instead of only applying the transient fault at the peak of grid signal, the transient fault was applied in different time of one fundamental frequency period, which are ¼ cycle after peak (case 13-25), ½ cycle after peak (case 27-36), ¾ cycle after peak (case 37-48). The detailed testing results are summarized from Table III to Table VI. In the tests, the normal system frequency is equal to 60 Hz. During the system transient faults, the frequency measurement errors are calculated by:

Frequency errors = measured frequency -60 (Hz)

During system transients, the measured frequency can be higher and lower than 60 Hz, therefore both the positive errors and negative errors are calculated and provided in each table.

**Table III Measurement errors (Case 1 - 12)** 

	Fault-tolera	nt Algorithm	Classical Algorithm	
	Positive errors (Hz)	Negative errors (Hz)	Positive errors (Hz)	Negative errors (Hz)
Case 1	0.123	-0.032	0.577	-16.048
Case 2	0.119	-0.030	2.493	-2.608
Case 3	0.116	-0.023	2.007	-2.116
Case 4	0.109	-0.019	1.514	-1.611
Case 5	0.094	-0.019	1.009	-1.089
Case 6	0.238	-0.063	0.499	-0.552
Case 7	0.124	-0.034	0.563	-23.960
Case 8	0.123	-0.036	0.509	-23.809
Case 9	0.119	-0.035	0.451	-16.965
Case 10	0.115	-0.034	0.382	-17.360
Case 11	0.106	-0.047	1.208	-1.288
Case 12	0.091	-0.367	0.666	-0.733

Table IV Measurement errors (Case 13 - 24)

	Fault-tolerant Algorithm		Classical Algorithm	
	Positive errors (Hz)	Negative errors	Positive errors	Negative errors
		(Hz)	(Hz)	(Hz)
Case 13	0.120	-0.029	2.953	-3.058
Case 14	0.118	-0.029	2.487	-2.590
Case 15	0.114	-0.027	2.002	-2.101
Case 16	0.111	-0.022	1.509	-1.600
Case 17	0.097	-0.027	1.003	-1.079
Case 18	0.272	-0.069	0.496	-0.544
Case 19	0.123	-0.034	0.236	-23.547
Case 20	0.121	-0.031	0.212	-23.433
Case 21	0.118	-0.032	0.188	-16.900
Case 22	0.113	-0.032	0.163	-17.305
Case 23	0.102	-0.037	1.208	-1.299
Case 24	0.101	-0.402	0.664	-0.741

Table V Measurement errors (Case 25 - 36)

	Fault-toleran	t Algorithm	Classical A	Algorithm
	Positive errors (Hz)	Negative errors	Positive errors	Negative errors
		(Hz)	(Hz)	(Hz)
Case 25	0.123	-0.032	2.958	-3.078
Case 26	0.119	-0.031	2.493	-2.608
Case 27	0.116	-0.023	2.007	-2.116
Case 28	0.109	-0.019	1.514	-1.612
Case 29	0.095	-0.019	1.009	-1.089
Case 30	0.238	-0.063	0.499	-0.552
Case 31	0.124	-0.034	0.104	-16.136
Case 32	0.123	-0.036	2.452	-2.547
Case 33	0.119	-0.035	2.054	-21.093
Case 34	0.115	-0.034	1.651	-20.686
Case 35	0.106	-0.047	1.208	-1.288
Case 36	0.091	-0.368	0.666	-0.733

Table VI Measurement errors (Case 37 - 48)

	Fault-toleran	t Algorithm	Classical A	Algorithm
	Positive errors (Hz)	Negative errors	Positive errors	Negative errors
		(Hz)	(Hz)	(Hz)
Case 37	0.120	-0.029	2.953	-21.988
Case 38	0.118	-0.029	2.487	-21.520
Case 39	0.114	-0.027	2.002	-2.101
Case 40	0.111	-0.022	1.509	-1.600
Case 41	0.097	-0.027	1.003	-1.079
Case 42	0.272	-0.069	0.496	-0.544
Case 43	0.123	-0.034	0.611	-16.442
Case 44	0.121	-0.031	0.524	-16.739
Case 45	0.118	-0.032	0.446	-29.647
Case 46	0.112	-0.032	1.654	-20.698
Case 47	0.102	-0.037	1.208	-1.299
Case 48	0.101	-0.402	0.664	-0.741

Further, the data are also plotted in Figure 44 to Figure 47. In each figure, the green bar represents the measurement errors of the fault-tolerant algorithm while the blue bar represents the measurement errors of the classical algorithm. Each bar ranges from negative to positive. In each plot, two red threshold lines at 1 and -3 are plotted as 61 Hz is the upper limit of trigger frequency while 57 Hz is the lower limit of trigger frequency. As we can observe from the four figures, the measurement errors of the fault-tolerant algorithm are far less than the errors of the classical algorithm. More importantly, we can see the errors of the fault-tolerant algorithm are all below the thresholds (i.e., 1Hz, and -3Hz) while for most of cases, the measurement errors of the classical algorithm are over the thresholds, and in about 50% cases, the measurement errors of the classical algorithm are far over the thresholds. In other words, if the measurement errors are greater than any of the two thresholds (1 or -3), it could cause mis-trip of DERs, and it was exactly what happened in the blue cut fire event on Aug. 2016 (700 MW utility scale solar falsely tripped due to incorrect frequency measurements). With the help of the fault-tolerant algorithm, the similar events can be prevented due to its robust measurement performance under system transient faults.

#### Milestone #3

In this project, our Milestone # 3 is "prevention of false tripping by the developed algorithm at a success rate of over 90% by applying transient faults using IEEE benchmark system model."

Among the 48 tests we performed, the number of cases whose frequency measurement errors by the classical algorithm are greater than 1 Hz or lower than -3 Hz is 40 while the number of cases whose frequency measurement errors by the fault-tolerant algorithm are greater than 1 Hz or lower than -3 Hz is 0. Therefore, the success rate of fault-tolerant algorithm is calculated as follows:

$$r_{success} = \frac{40}{40} = 100 \%$$

The success rate is 100 % and meets the 90% success rate in milestone #3.

Milestone	Metric Definition	Finished Date	Success Value
3	prevention of false tripping by the developed algorithm at a success rate of over 90% by applying transient faults using IEEE benchmark system model.	6/30/2019	100%

Without the fault-tolerant algorithm, the false trip rate of the classical algorithm is 40/48 = 83.33 % while the false trip rate of the fault-tolerant algorithm is 0/48 = 0 %. Therefore, we can conclude that the fault-tolerant algorithm performs very well under system transients and can contribute to prevent false trip of DERs due to incorrect frequency measurements.

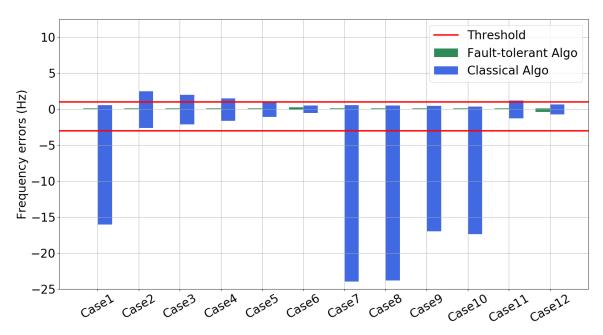


Figure 44 IEEE system model case 1 - 12

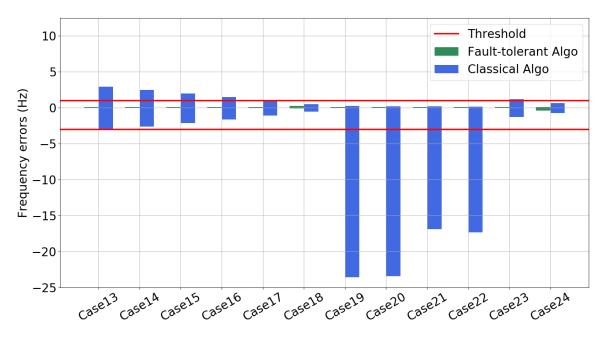


Figure 45 IEEE system model case 13 - 24

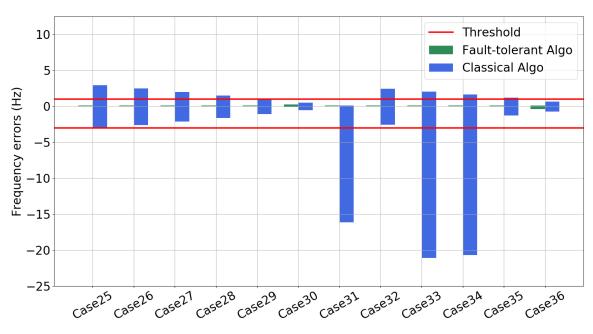


Figure 46 IEEE system model case 25 - 36

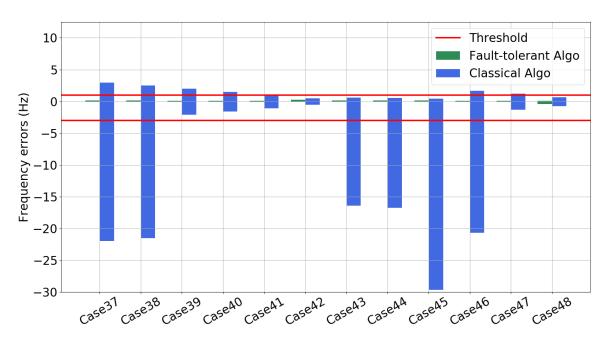


Figure 47 IEEE system model case 37 - 48

The time domain testing results of case 1, 13, 25, and 37 are shown in Figure 48 to Figure 51, and all other cases are provided in Appendix A. The blue and green waveforms represent the frequency measurements of classical algorithm and fault-tolerant algorithm, respectively. The fault-tolerant measurement algorithm has significantly less measurement errors during the transients. The black waveform represents the raw and point-on-wave grid signal with a fault occurring at about 0.2 second and cleared about 3 cycles later. The red waveforms represent the system transient detection (0 indicates no transient detected, and 1 indicates a transient detected), and we can see that the transient detector successfully detected the system transient when the fault occurs and clears.

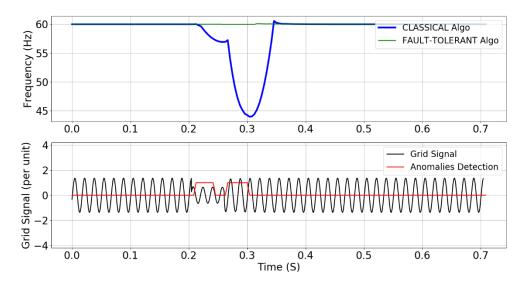


Figure 48 time domain result – case 1

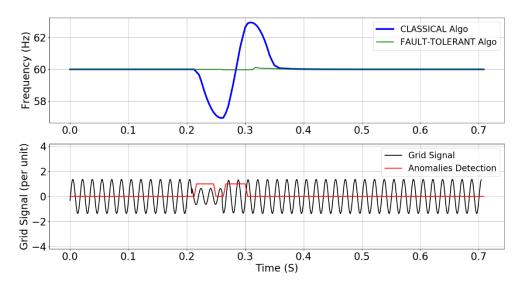


Figure 49 time domain result – case 13

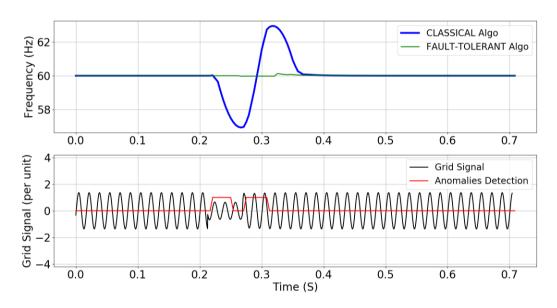


Figure 50 time domain result – case 25

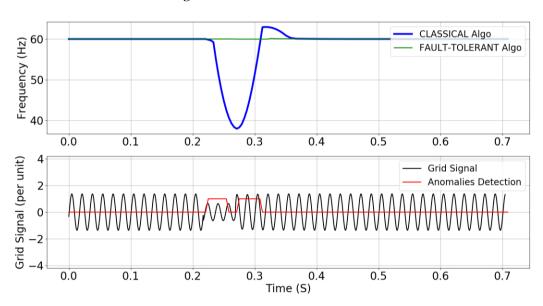


Figure 51 time domain result – case 37

### 5.1.4.2 Security test of fault-tolerant algorithm

Now we have tested the dependability performance of the fault-tolerant algorithm under system transient faults. By performing dependability tests, we demonstrated that the fault-tolerant measurement algorithm can successfully detect the occurrence of the system transients and take actions to significantly mitigate measurement errors.

Besides the dependability performance, another important performance of the fault-tolerant algorithm is security performance. The power grid could experience some other type of distortions such as oscillation, ramping up and down, harmonics, etc. For these distortions, although the power grid signals are not ideal sinusoidal, the state-of-the-art algorithm can usually accurately perform the measurements, so the transient detector of the fault-tolerant algorithm should not trigger and recognize these distortions as "fault". Although trigger under these conditions will not significantly cause large measurement errors as the measurements are locked to latest valid measurement, it will fail to provide real-time valid measurements. Therefore, the fault-tolerant algorithm shall not "kick-in" when the grid experiences the distortions when the classical algorithm can perform well.

The IEEE Standard C37.118.1-2011 [22][18] and C37.118.1a-2014 [25].[25] provides a reference for such tests. It has various test signals that classical algorithm shall handle well. A detailed introduction of each test and the testing results are introduced below.

#### Harmonic test

The signal is represented as follows:

$$s(t) = \cos(2\pi f t) + 0.1\cos(2\pi i f t)$$

where s(t) represents the test signal;  $\cos(2\pi f t)$  is the fundamental component of the test signal; f is the fundamental frequency of the grid signal, i is the order of the harmonic component, and 10% harmonic component, which is much higher than real-world grid environment, is assumed.

The testing results are shown in Figure 52 to Figure 63 with different combinations of f and i. We can see the frequency measurements of the classical and fault-tolerant algorithm can match well each other, and it can be concluded that the fault-tolerant frequency algorithm performs stably and did not mistakenly recognize harmonic as transient faults.

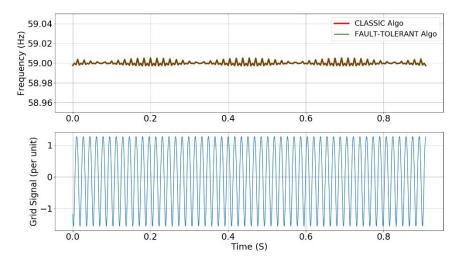


Figure 52 harmonic test (f = 59, i = 2)

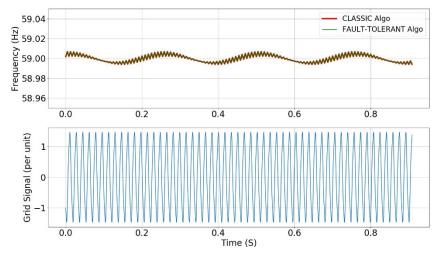


Figure 53 harmonic test (f = 59, i = 3)

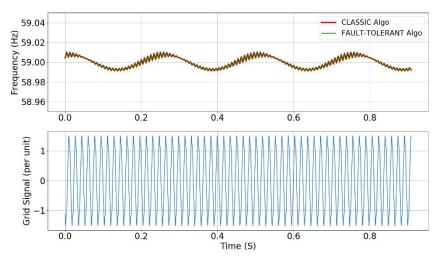


Figure 54 harmonic test (f = 59, i = 5)

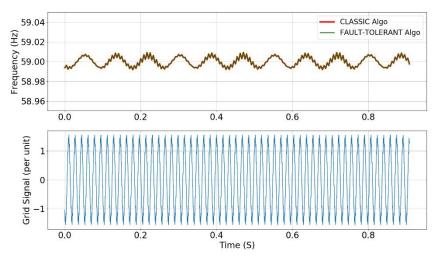


Figure 55 harmonic test (f = 59, i = 7)

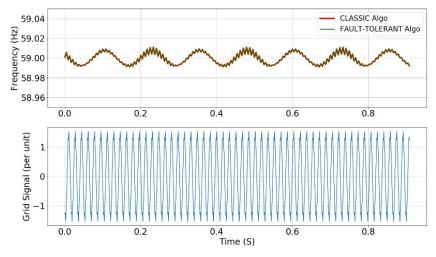


Figure 56 harmonic test (f = 59, i = 11)

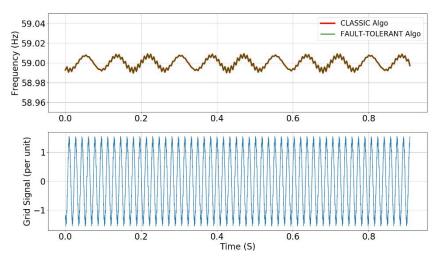


Figure 57 harmonic test (f = 59, i = 13)

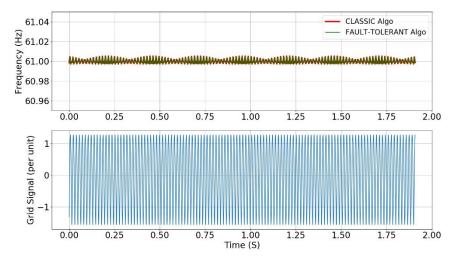


Figure 58 harmonic test (f = 61, i = 2)

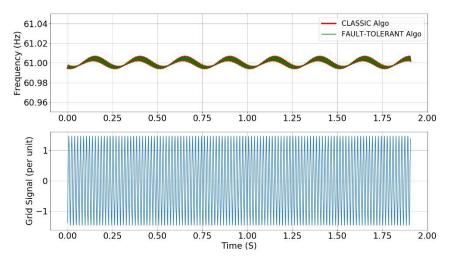


Figure 59 harmonic test (f = 61, i = 3)

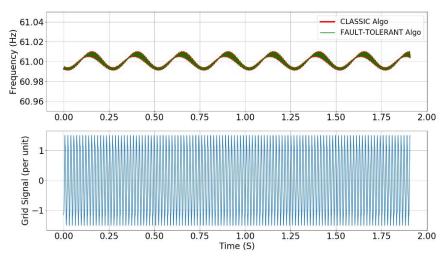


Figure 60 harmonic test (f = 61, i = 5)

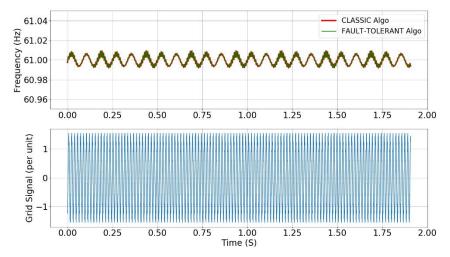


Figure 61 harmonic test (f = 61, i = 7)

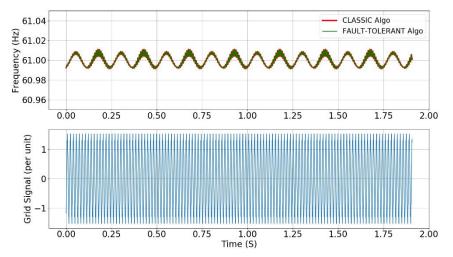


Figure 62 harmonic test (f = 61, i = 11)

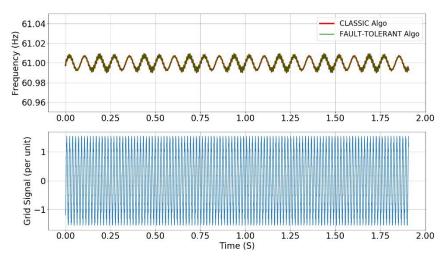


Figure 63 harmonic test (f = 61, i = 13)

## Phase angle modulation test

The signal is represented as follows:

$$s(t) = 1\cos(2\pi f_0 t + m_a \cos(2\pi f_a t - \pi))$$

where s(t) represents the test signal;  $f_0$  is the nominal frequency of the grid signal (i.e., 60 Hz in United States),  $m_a$  is the magnitude of the modulation signal, and  $f_a$  is the modulation frequency of the modulation signal.

The testing results are shown in Figure 64 to Figure 70 with different combinations of  $m_a$  and  $f_a$ . We can see the frequency measurements of the classical and fault-tolerant algorithm can match well each other, and it can be concluded that the fault-tolerant frequency algorithm performs stably and did not mistakenly recognize phase modulation signals as transient faults.

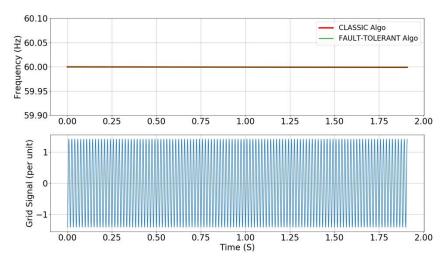


Figure 64 Phase modulation test  $(m_a = 0.01, f_a = 0.1)$ 

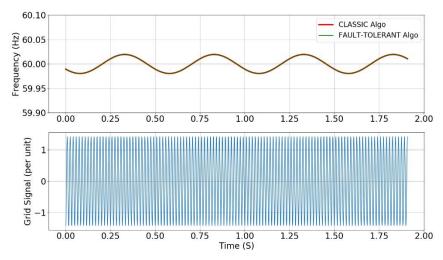


Figure 65 Phase modulation test  $(m_a = 0.01, f_a = 2)$ 

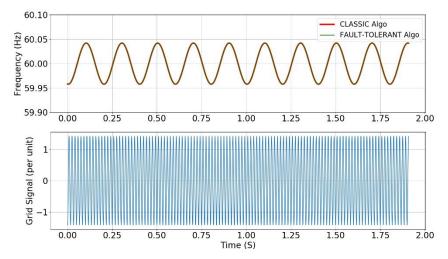


Figure 66 Phase modulation test  $(m_a = 0.01, f_a = 5)$ 

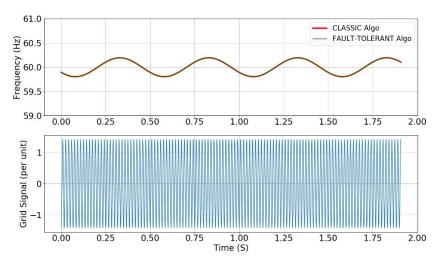


Figure 67 Phase modulation test  $(m_a = 0.1, f_a = 2)$ 

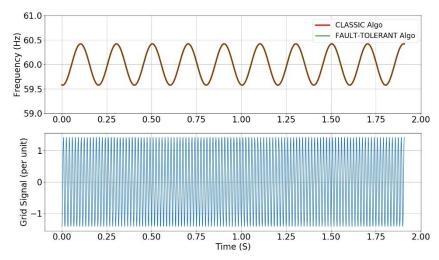


Figure 68 Phase modulation test  $(m_a = 0.1, f_a = 5)$ 

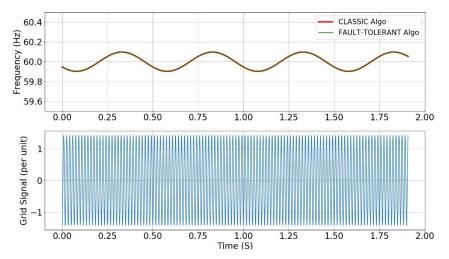


Figure 69 Phase modulation test  $(m_a = 0.1, f_a = 5)$ 

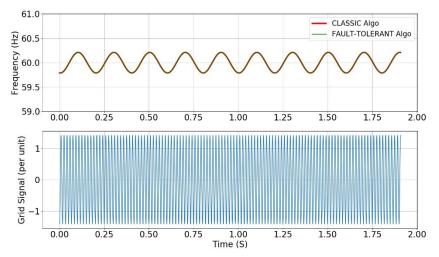


Figure 70 Phase modulation test  $(m_a = 0.05, f_a = 5)$ 

### Magnitude modulation test

The signal is represented as follows:

$$s(t) = (1 + m_m \cos(2\pi f_m t)) * \cos(2\pi f_0 t)$$

where s(t) represents the test signal;  $f_0$  is the nominal frequency of the grid signal (i.e. 60 Hz in United States),  $m_a$  is the magnitude of the modulation signal, and  $f_m$  is the modulation frequency of the modulation signal.

The testing results are shown in Figure 71 to Figure 79 with different combinations of  $m_m$  and  $f_m$ . We can see the frequency measurements of the classical and fault-tolerant algorithm can match well each other, and it can be concluded that the fault-tolerant frequency algorithm performs stably and did not mistakenly recognize magnitude modulation signals as transient faults.

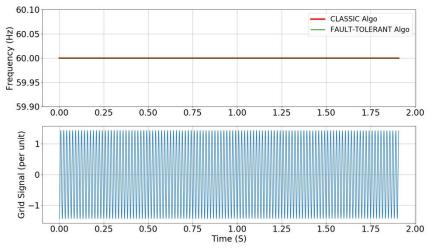


Figure 71 Magnitude modulation test  $(m_m = 0.01, f_m = 0.1)$ 

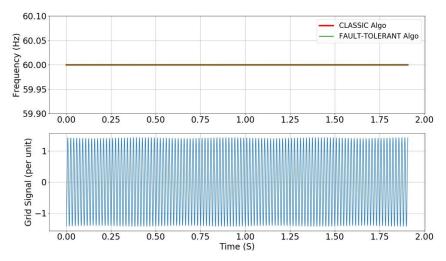


Figure 72 Magnitude modulation test ( $m_m = 0.01, f_m = 2$ )

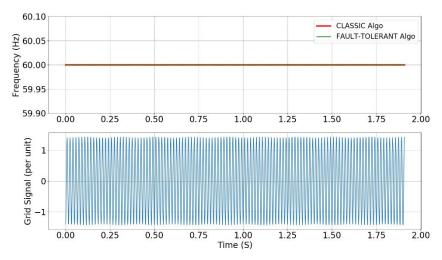


Figure 73 Magnitude modulation test ( $m_m = 0.01, f_m = 5$ )

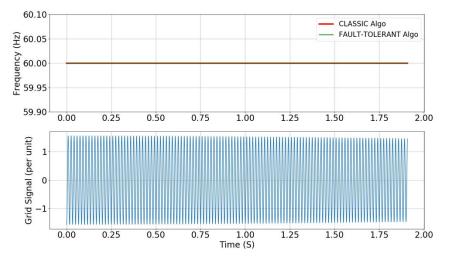


Figure 74 Magnitude modulation test  $(m_m = 0.1, f_m = 0.1)$ 

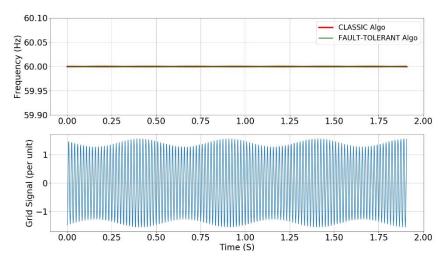


Figure 75 Magnitude modulation test  $(m_m = 0.1, f_m = 2)$ 

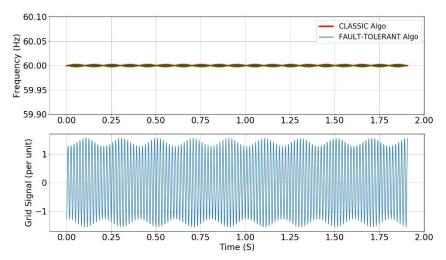


Figure 76 Magnitude modulation test  $(m_m = 0.1, f_m = 5)$ 

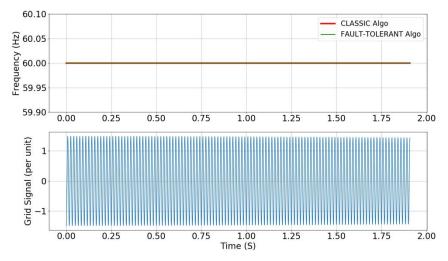


Figure 77 Magnitude modulation test ( $m_m = 0.05, f_m = 0.1$ )

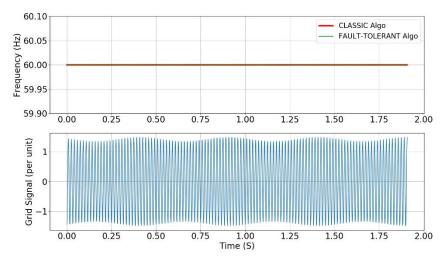


Figure 78 Magnitude modulation test ( $m_m = 0.05, f_m = 2$ )

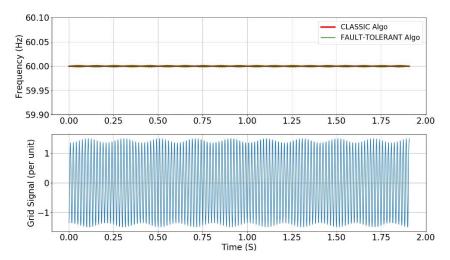


Figure 79 Magnitude modulation test ( $m_m = 0.05, f_m = 5$ )

### Magnitude ramping test

Note: this test is not specified in IEEE Std., however we believe it is a practical scenario that can happen in real-world, so we did the test.

$$s(t) = (M_s + r * t)\cos(2\pi f t)$$

where  $M_s$  is the initial magnitude at t = 0; r is the rate of magnitude ramping.

The testing results are shown in Figure 80 to Figure 83 with different combinations of  $M_s$  and r. We can see the frequency measurements of the classical and fault-tolerant algorithm can match well each other, and it can be concluded that the fault-tolerant frequency algorithm performs stably and did not mistakenly recognize magnitude ramping signals as transient faults.

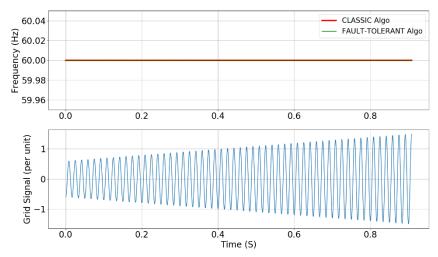


Figure 80 Magnitude ramping test  $(M_s = 0.5, r = 1, f = 60)$ 

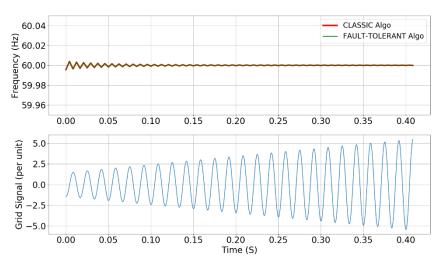


Figure 81 Magnitude ramping test  $(M_s = 0.5, r = 5, f = 60)$ 

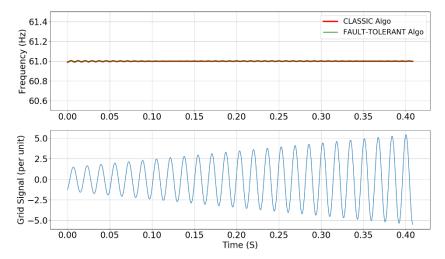


Figure 82 Magnitude ramping test  $(M_s = 0.5, r = 5, f = 61)$ 

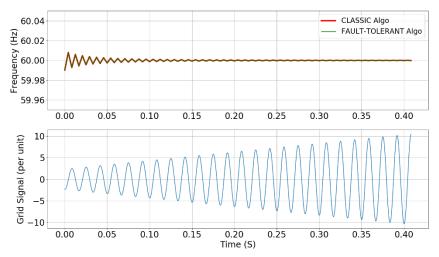


Figure 83 Magnitude ramping test  $(M_s = 0.5, r = 10, f = 60)$ 

### Frequency ramping test

$$s(t) = 1\cos(2\pi(59 + 0.5t)t)$$

In this test, the frequency first ramps up from 59 Hz to 61 Hz in 2 seconds, and stays at 61 for a second, then ramps down from 61 Hz to 59 Hz in 2 seconds. We can see the frequency measurements of the classical and fault-tolerant algorithm can match well each other, and it can be concluded that the fault-tolerant frequency algorithm performs stably and did not mistakenly recognize frequency ramping signals as transient faults.

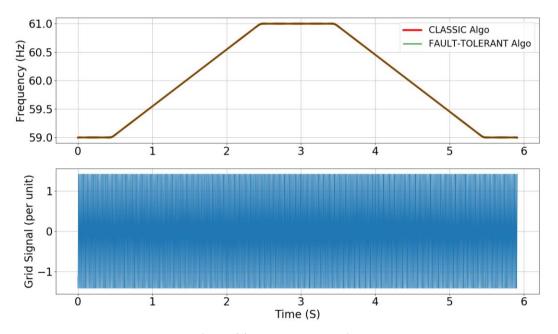


Figure 84 Frequency ramping test

#### 5.2 COMPUTATION COMPLEXITY ANALYSIS AND COMPUTATION COST TEST

To analyze the applicability of the new algorithm on industry products, the computational complexity of the algorithm was analyzed in detail in terms of number of numerical operations, and the computation capacity of industry widely used microprocessor Texas Instruments TMS320VC33 is used for analysis, which has 150 million Floating-Point Operations Per Second (MFLOPS). The computation time of the algorithm was tested by considering different combination of sampling rate, window size.

The proposed fault-tolerant estimator can be divided into two parts. The first part is the transient detector, and the second part is the frequency estimator. They will be analyzed separately.

#### **5.2.1** Transient Detector

### **5.2.1.1** Computational Complexity of The Transient Detector

A flowchart of the transient detector is shown in Figure 85.

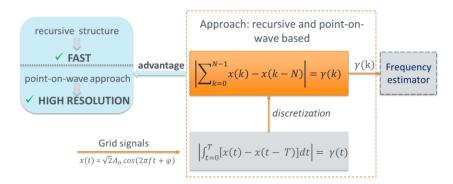


Figure 85 Flowchart of the transient detector

The transient detector runs in real-time at a rate of sampling rate, which can be expressed by

$$\gamma(i) = \left| \sum_{k=0}^{N-1} x(i+k) - x(i+k-N) \right|$$
 (10)

where x is the sampled value of the power grid electric waveforms, N is the number of sampled values in one fundamental frequency cycle (i.e., 1/60 second for a 60 Hz power system). The computation time of (10) is proportional to N, which will increase with the sampling rate. However, assuming

$$s(i) = \sum_{k=0}^{N-1} x(i+k) - x(i+k-N)$$
 (11)

(11) can be rewritten in the following form

$$s(i) = \begin{cases} s(i-1) + \{x(i+N) - x(i)\} - \{x(i) - x(i-N)\}, & i > 0 \\ \sum_{k=0}^{N-1} x(k) - x(k-N), & i = 0 \end{cases}$$
 (12)

According to (12), the number of numerical operations to calculate s(0) is proportional to N. Once we have s(0), s(i) can be calculated recursively. To be specific, to calculate s(i) from s(i-1), we only one

"addition", and three "subtraction". And to calculate  $\gamma(i)$  from s(i), one 'abs' operation is needed. Table VII summarizes the count of numerical operations of the transient detector.

Table VII Count of numerical operations of the transient detector

	count of "+" operation	count of "-" operation	count of "abs" operation
Transient detector	1	3	1

According to the analysis and Table VII, we can conclude that the computational complexity of the transient detector in Big  $\theta$  notation is  $\theta$ (1), which is constant time and independent of sampling rate or computation window. As only a few numbers of numerical operations are needed, the computation will be very fast.

The modern digital signal processors can perform one or more numerical operations (e.g., `+`, `-`, and `abs`) per microprocessor cycle, thus, a new transient detector result can be computed within a few microprocessor cycles.

### 5.2.1.2 Computation Time Analysis on Digital Signal Processor

Digital Signal Processor (DSP) is usually used for measurement computation. Table VIII shows the calculation of the computation utilization rate of the transient detector on a typical DSP. Assuming the sampling rate is 6000 Hz, the total number of numerical operations of the transient detector is about 30,000, which is only 0.02 % of the computing capability of the DSP.

Table VIII Computation utilization rate of the transient detector

	# of numerical operations per measurement	# of measurements per second	Total # of numerical operations per second	MFLOPS of the DSP	Computation utilization rate
Transient	5	6000	30,000	150,000,000	0.02%
Detection					

### **5.2.1.3** Computation Time Test of The Transient Detector

The computation time of the transient detector has also been evaluated on a 3.6 GHz 4-core microprocessor, and the testing results are shown in Table IX.

Different combinations of sampling rate and time of test signal are tested. According to Table IX, the total computation time increases with the sampling rate and time of test signal, which is expected. The key observation is the computation time per calculation in last column, which is almost constant regardless of sampling rate or time of test signal. It varies in a narrow range from 70 to 90 microsecond. At a sampling rate of 5760 Hz, the computation time is about 0.9 microsecond. The low computation cost is very critical for real-time transient event detection.

Table IX Computation time test of the transient detector

G 1'	Time of the state of	NI	Computation Time	
Sampling Rate	Time of test signal (Second)	Number of sampled values	Total (Second)	Per calculation (Microsecond)
	1	1,440	0.001	0.73
1440 Hz	10	14,400	0.012	0.84
	100	144,000	0.123	0.86
	1	2,880	0.002	0.72
2880 Hz	10	28,800	0.023	0.80
	100	288,000	0.244	0.85
	1	5,760	0.005	0.90
5760 Hz	10	57,600	0.046	0.80
	100	576,000	0.488	0.85

# 5.2.2 Frequency Estimator

# 5.2.2.1 Computational Complexity of The Frequency Estimator

The frequency estimator is more complex than the transient detector. For a frequency estimation, the first step is to compute the Discrete Fourier Transform (DFT) using one fundamental frequency data. The time interval between two DFT is determined by the frequency estimate rate. Assuming the frequency is computed every quarter cycle (1/240 seconds), then the DFT needs to be computed every quarter cycle. For each DFT, it can be expressed by

$$\begin{cases} X_r = \frac{1}{\sqrt{2}N} \sum_{k=0}^{N-1} x_k \cos\left(\frac{2\pi k}{N}\right) \\ X_i = \frac{1}{\sqrt{2}N} \sum_{k=0}^{N-1} x_k \sin\left(-\frac{2\pi k}{N}\right) \end{cases}$$
(13)

The cos form and sin form in (13), together  $\frac{1}{\sqrt{2}} \frac{2}{N}$  are constants, so they can be computed offline and saved in the memory of microprocessor. Therefore, the number of "\*" operation and '+' operation for  $X_r$  and  $X_i$  is N, and (N-1), respectively.

With  $X_i$  and  $X_r$ , the phase angle can be computed by

$$\varphi = \arctan\left(\frac{X_i}{X_r}\right) \tag{14}$$

As frequency is the derivate of phase angle, then we have

$$\varphi'(m) = \frac{\varphi(m) - \varphi(m-1)}{\Lambda t} = 2\pi f(m), m \ge 1$$
 (15)

where m is the sequence index of phase angle, f(m) is the estimated grid frequency, and  $\Delta t$  is the time interval between two phase angles. According to (15), the frequency can be computed by  $\varphi(m)$  and  $\varphi(m-1)$ . However, the power grid waveforms are always distorted by a variety of disturbances such as noise, harmonics, oscillation, etc. Therefore, in practice, usually a series of phase angles are used for the frequency estimation.

First, assuming the number of phase angles used for one frequency estimation is (2L+1). A quadratic polynomial is used to fit these phase angles, and the quadratic polynomial is expressed by

$$p(i) = \alpha_0 + \alpha_1 t(i) + \alpha_2 t(i)^2, i = 0, ..., 2L$$
(16)

where  $\alpha_0$ ,  $\alpha_1$ ,  $\alpha_2$  are the coefficients of the quadratic polynomial,  $t(i) = (i - L)\Delta t$ .

The derivate of p(i) can be expressed by

$$p'(i) = \alpha_1 + 2\alpha_2 t(i). \tag{17}$$

Further, at sample index i = L,

$$p'(L) = \alpha_1. \tag{18}$$

Therefore, the frequency at sample index i = L can be calculated by

$$f = \frac{\alpha_1}{2\pi}. (19)$$

According to (19), we only need estimate  $\alpha_1$  to estimate frequency. The curve fitting errors are minimized to estimate  $\alpha_1$ . Using the estimated phase angle  $\varphi(i)$  and the fitting curve p(i), the sum of the square of the error can be expressed by

$$e = \epsilon(\alpha_0, \alpha_1, \alpha_2) = \sum_{i=0}^{2L} (p(i) - \varphi(i))^2$$
. (20)

 $e = \epsilon(\alpha_0, \alpha_1, \alpha_2) = \sum_{0}^{2L} (p(i) - \varphi(i))^2$ . (20) To minimize the error e, the partial derivatives of e with respect to the coefficient  $\alpha_0, \alpha_1, \alpha_2$  should be equal to zero, respectively.

The equation resulting from evaluating the partial derivative of e with respect to  $\alpha_0$ ,  $\alpha_1$ , and  $\alpha_2$  are presented as follows.

$$\frac{\partial e}{\partial \alpha_0} = 2\alpha_0(2L+1) + 2\alpha_1 \sum_{i=0}^{2L} t(i) + 2\alpha_2 \sum_{i=0}^{2L} t(i)^2 - 2\sum_{i=0}^{2L} \varphi(i)$$
 (21)

$$\frac{\partial e}{\partial \alpha_i} = 2\alpha_0 \sum_{i=0}^{2L} t(i) + 2\alpha_1 \sum_{i=0}^{2L} t(i)^2 + 2\alpha_2 \sum_{i=0}^{2L} t(i)^3 - 2\sum_{i=0}^{2L} \varphi(i)t(i)$$
 (22)

$$\frac{\partial e}{\partial \alpha_0} = 2\alpha_0(2L+1) + 2\alpha_1 \sum_{i=0}^{2L} t(i) + 2\alpha_2 \sum_{i=0}^{2L} t(i)^2 - 2\sum_{i=0}^{2L} \varphi(i) \qquad (21)$$

$$\frac{\partial e}{\partial \alpha_1} = 2\alpha_0 \sum_{i=0}^{2L} t(i) + 2\alpha_1 \sum_{i=0}^{2L} t(i)^2 + 2\alpha_2 \sum_{i=0}^{2L} t(i)^3 - 2\sum_{i=0}^{2L} \varphi(i)t(i) \qquad (22)$$

$$\frac{\partial e}{\partial \alpha_2} = 2\alpha_0 \sum_{i=0}^{2L} t(i)^2 + 2\alpha_1 \sum_{i=0}^{2L} t(i)^3 + 2\alpha_2 \sum_{i=0}^{2L} t(i)^4 - 2\sum_{i=0}^{2L} \varphi(i)t(i)^2 \qquad (23)$$

To error e is minimized when the derivatives are equal to zero (i.e.,  $\frac{\partial e}{\partial \alpha_0} = \frac{\partial e}{\partial \alpha_1} = \frac{\partial e}{\partial \alpha_2} = 0$ ), thus,

$$\begin{bmatrix} (2L+1) & \sum_{i=0}^{2L} t(i) & \sum_{i=0}^{2L} t(i)^{2} \\ \sum_{i=0}^{2L} t(i) & \sum_{i=0}^{2L} t(i)^{2} & \sum_{i=0}^{2L} t(i)^{3} \\ \sum_{i=0}^{2L} t(i)^{2} & \sum_{i=0}^{2L} t(i)^{3} & \sum_{i=0}^{2L} t(i)^{4} \end{bmatrix} \begin{bmatrix} \alpha_{0} \\ \alpha_{1} \\ \alpha_{2} \end{bmatrix} = \begin{bmatrix} \sum_{i=0}^{2L} \varphi(i) \\ \sum_{i=0}^{2L} \varphi(i)t(i) \\ \sum_{i=0}^{2L} \varphi(i)t(i)^{2} \end{bmatrix}.$$
(24)

Because  $t(i) = (i - L)\Delta t$ 

$$\sum_{i=0}^{2L} t(i) = \sum_{i=0}^{2L} t(i)^3 = 0.$$
 (25)

Therefore, (24) can be rewritten as

$$\begin{bmatrix} (2L+1) & 0 & \sum_{i=0}^{2L} t(i)^{2} \\ 0 & \sum_{i=0}^{2L} t(i)^{2} & 0 \\ \sum_{i=0}^{2L} t(i)^{2} & 0 & \sum_{i=0}^{2L} t(i)^{4} \end{bmatrix} \begin{bmatrix} \alpha_{0} \\ \alpha_{1} \\ \alpha_{2} \end{bmatrix} = \begin{bmatrix} \sum_{i=0}^{2L} \varphi(i) \\ \sum_{i=0}^{2L} \varphi(i)t(i) \\ \sum_{i=0}^{2L} \varphi(i)t(i)^{2} \end{bmatrix}$$
(26)

Simply the above equation as

$$T\alpha = \varphi \tag{27}$$

Then, applying  $T^{-1}$  on both sides yields:

$$\alpha = T^{-1} \boldsymbol{\varphi} \tag{28}$$

where  $T^{-1}$  is the inverse of matrix T.

(28) can be expanded as

$$\begin{bmatrix} \alpha_0 \\ \alpha_1 \\ \alpha_2 \end{bmatrix} = \begin{bmatrix} T_{11} & T_{12} & T_{13} \\ T_{21} & T_{22} & T_{23} \\ T_{31} & T_{32} & T_{33} \end{bmatrix} \begin{bmatrix} \sum_{i=0}^{2L} \varphi(i) \\ \sum_{i=0}^{2L} \varphi(i)t(i) \\ \sum_{i=0}^{2L} \varphi(i)t(i)^2 \end{bmatrix}$$
(29)

where  $T_{ij}$  is the entry of matrix  $T^{-1}$ .

Because only  $\alpha_1$  is needed for frequency estimation, only  $T_{21}$ ,  $T_{22}$ , and  $T_{23}$  in  $T^{-1}$  need to be calculated, which are as follows:

$$T_{21} = -\frac{1}{\det(r^{-1})} \begin{vmatrix} 0 & \sum_{i=0}^{2L} t(i)^2 \\ 0 & \sum_{i=0}^{2L} t(i)^4 \end{vmatrix} = 0$$
 (30)

$$T_{22} = \frac{1}{\det(r^{-1})} \begin{vmatrix} 2L+1 & \sum_{i=0}^{2L} t(i)^{2} \\ \sum_{i=0}^{2L} t(i)^{2} & \sum_{i=0}^{2L} t(i)^{4} \end{vmatrix}$$

$$= \frac{(2L+1)\sum_{i=0}^{2L} t(i)^{4} - (\sum_{i=0}^{2L} t(i)^{2})^{2}}{\det(\mathbf{T}^{-1})}$$

$$T_{23} = -\frac{1}{\det(r^{-1})} \begin{vmatrix} 2L+1 & 0 \\ \sum_{i=0}^{2L} t(i)^{2} & 0 \end{vmatrix} = 0$$
(32)

$$T_{23} = -\frac{1}{\det(r^{-1})} \begin{vmatrix} 2L+1 & 0\\ \sum_{i=0}^{2L} t(i)^2 & 0 \end{vmatrix} = 0$$
 (32)

where  $\det(T^{-1})$  is the determinant of  $T^{-1}$ 

Since  $T_{21} = T_{23} = 0$ ,  $\alpha_1$  can be expressed by

$$\alpha_{1} = T_{21} \sum_{i=0}^{2L} \varphi(i) + T_{22} \sum_{i=0}^{2L} \varphi(i)t(i) + T_{23} \sum_{i=0}^{2L} \varphi(i)t(i)^{2}$$

$$= T_{22} \sum_{i=0}^{2L} \varphi(i)t(i) .$$
(33)

Note that  $T_{22}$  only depends on the parameters of L and  $\Delta t$ , which are constant parameters,  $T_{22} * t(i)$  can be calculated offline and saved in the memory of microprocessor.

Typically, using (19) and (33), a new frequency can be estimated using a total number of (2L + 1) phase angles. We can conclude that the computation time for a new frequency estimation is proportional to Lsince it requires (2L + 1) multiplications and 2L additions according to (33).

In practice, because the phase angle obtained by (14) is between  $-\pi$  to  $\pi$ . Thus, the angles may need to be unwrapped for least squares fitting. The example code of unwrapping is provided below.

```
def unwrap(angles, limit):
   def is_wrap(cur, pre):
   unwrap_angles = [angles[0]]
        if is_wrap(angles[i], angles[i-1]):
            if angles[i] < 0:
       unwrap_angles.append(angles[i] + pi_2*off_set)
```

Table X summarizes the count of numerical operations for each part. The total numerical operation is equal to (4N + 22L + 1). Assuming the sampling rate is 5760 Hz (i.e., N = 96), and the L is equal to 9, the total number of numerical operations is equal to 295.

Table X Count of numerical operations of frequency estimator

	count of "+" and "-"	count of "*"	count of "/"	count of 'abs'	count of '>' or '<'	count of "arctan"
Angle computation by DFT	2(N-1)	2N	0	0	0	1
Phase Angle Unwrapping	6L	4L	0	2L	6L	0
Frequency computation by Least Squares Fitting - Polynomial	2L	2L+1	1	0	0	0
Total	2(N-1) + 8L	2N + 6L+1	1	2L	6L	1

# 5.2.2.2 Computation Time Analysis of the Frequency Estimator on Digital Signal Processor

Similar as the analysis of the transient detector, Table XI shows the computation utilization rate of the frequency estimation on the DSP. The rate is only 0.05 %, which is very low. It means that the computational cos of the frequency estimator is very low for a modern DSP.

Table XI Computation utilization rate of the frequency estimator

	# of numerical operations per measurement	# of measurements per second	Total # of numerical operations per second	MFLOPS of the DSP	Computation Utilization Rate	
Frequency Estimation	295	240	70,800	150,000,000	0.05%	

# **5.2.2.3** Computation Time Simulation Test of The Frequency Estimator

The computation time of the frequency estimator has also been evaluated on a 3.6 GHz 4-core microprocessor.

Different combinations of sampling rate, window length and time of test signal have been evaluated to perform the computation time testing. The testing results are shown in Table XII, Table XIII, and Table XIV. For a specific sampling rate, the frequency computation time is mainly determined by the window length. The computation time for per frequency calculation increases by about 10 microseconds from a shorter window (2.5 cycle) to a typical window size (5.5 cycle). The computation time increase is trivial. However, a longer window can contribute to better noise rejection performance. Table XV, Table XVI, and Table XVII show the frequency measurement errors under different sampling rate and window size. It could be seen that compared to the sampling rate, a longer window size is preferred to mitigate the measurement errors. At the sampling rate of 1440 Hz and window size of 5.5-cycle, the maximum frequency measurement error under 60 dB noise is below 2 Millihertz (0.002 Hz), which is accurate enough for practical applications. In this case, the computation time is about 40 microseconds (0.00004 seconds) for a frequency computation. Note that the frequency measure rate is a quarter cycle, that is 1/240 second for a 60 Hz power grid. It means we estimate a frequency every 1/240 second.

Table XII Computation time test of the frequency estimator (sampling rate = 1440 Hz)

Complina	Window	Time of test	Computation Tin	ne
Sampling Rate	length(=L/2+1)	signal	Total (Second)	Per calculation (Microsecond)
	2.5	1	0.007	31
		10	0.072	30
		100	0.76	32
		1	0.008	36
1440 Hz	4	10	0.083	35
		100	0.868	36
5.5	1	0.009	42	
	5.5	10	0.094	40
		100	0.973	41

Table XIII Computation time test of the frequency estimator (sampling rate = 2880 Hz)

Compling	Window	Time of test	Computation Tim	ne
Sampling Rate	length(=L/2+1)	signal	Total (Seconds)	Per calculation
			Total (Seconds)	(Microsecond)
	2.5	1	0.011	48
		10	0.112	47
		100	1.12	47
		1	0.012	54
2880 Hz	4	10	0.122	51
		100	1.23	51
		1	0.012	55
	5.5	10	0.132	56
		100	1.355	56

Table XIV Computation time test of the frequency estimator (sampling rate =  $5760 \ Hz$ )

Sampling Rate	Window length(=L/2+1)	Time of test signal	Computation Time	
			Total (Second)	Per calculation (Microsecond)
5760 Hz	2.5	1	0.019	83
		10	0.191	80
		100	1.922	80
	4	1	0.019	85
		10	0.201	84
		100	2.081	87
	5.5	1	0.020	92
		10	0.213	89
		100	2.115	88

Table XV Frequency measurement errors vs window size (sampling rate = 1440 Hz)

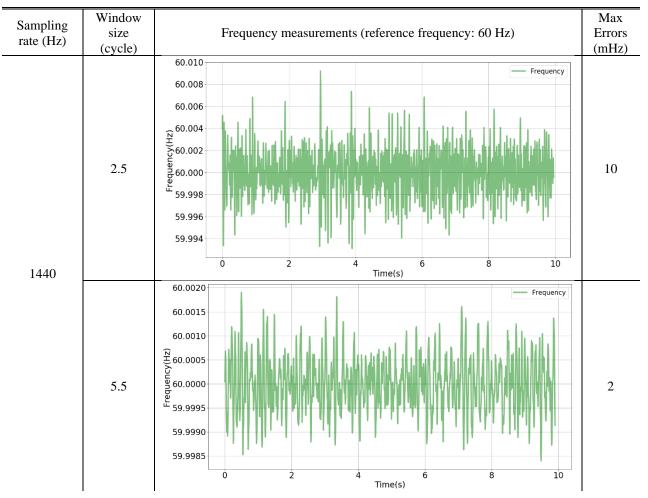


Table XVI Frequency measurement errors vs window size (sampling rate = 2880 Hz)

Sampling rate (Hz)	Window size (cycle)	Frequency measurements (reference frequency: 60 Hz)		
2880	2.5	60.006 60.004 (2H) 60.002 59.998 59.996 0 2 4 6 8 10	6	
	5.5	60.0010 (AH)/Supple	1.5	

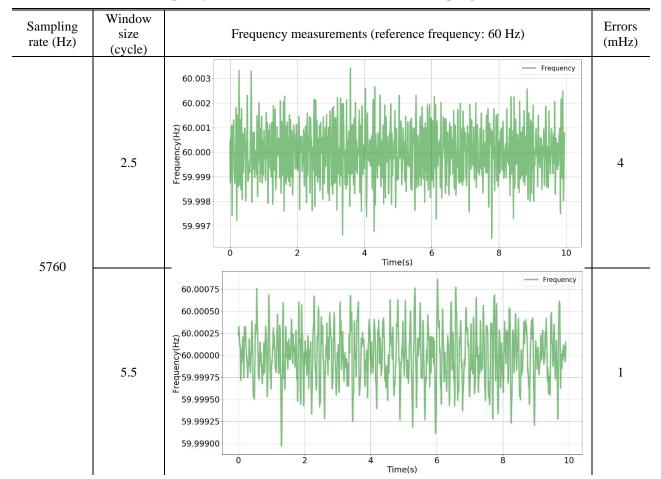


Table XVII Frequency measurement errors vs window size (sampling rate = 5760 Hz)

### 5.3 HARDWARE VALIDATION

In this section, the proposed fault-tolerant frequency measurement algorithm was successfully realized on a low-cost hardware, and it can work in real-time measurement. To verify the effectiveness and low computation cost of the algorithm, the algorithm has been implemented on hardware to demonstrate its feasibility. For the transient detector, the core of the algorithm, low-cost hardware and microprocessor are used to verify its low computational cost, and the microprocessor is an ARM 80 MHz MCU. A series of testing waveforms were applied to test the performance. The results showed that during a fault period, the frequency measurement output had much small deviation from the steady state output. In this section, the results obtained from the test experiments were provided.

# 5.3.1 Experiment setup for fault-tolerant frequency measurement

The developed measuring system in the project included the fault detector and a tablet with an app to perform frequency computation and visualization. The fault detector sampled the voltage signal with high-precision GPS timing synchronization and performed transient event detection. At the same time, the fault detector transferred the sample data and fault detection result to the tablet. The frequency computation app running on the tablet processed the received data to determine whether it was necessary to trigger frequency lock and the adaptive frequency computation. In addition to the displaying on the tablet, an oscilloscope was used as a comparison for displaying the output waveform of the calibrator.

Figure 86 presented the experiment setup for testing the developed frequency measurement and visualization system. The testing power signal was generated from the Fluke 6135A/PMUCAL PMU Calibration system, which is the only fully automated and traceable PMU calibration system available today.



Figure 86 Experiment setup for fault-tolerant frequency measurement

# 5.3.2 Hardware testing

# 5.3.2.1 Frequency measurement tests under normal grid operation

# Connected to power grid

In this experiment, the measuring system was connected to the power grid via 120-V power outlet in laboratory. In this way, the real power grid data was feed into the measuring system. The measurement results are shown in Figure 87. As there is no system transient occurred, the computed frequency and RMS value of the PMU calibrator is very stable and fluctuate in a small range. The transient detection is 0 over the testing, and it demonstrated that the transient detector will not be mistakenly triggered by the normal grid waveform distortions such as noise, harmonics, etc. The small measurement variation could verify the frequency measurements are very stable in real-grid measurement environment and have good grid disturbance rejection performance.

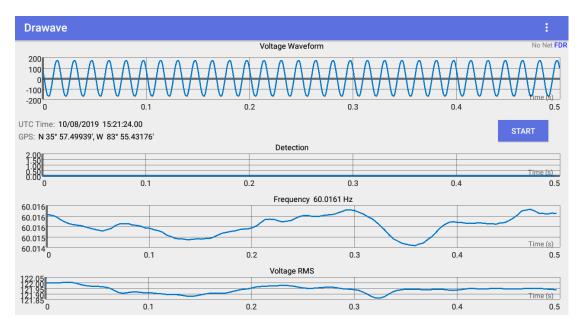


Figure 87 Real-time measurements of power grid (via 120-V power outlet)

#### Connected to PMU calibrator

In this test, the measuring system was connected to the PMU calibrator, and 60 Hz clean signal was generated from the PMU calibrator and used for the measuring system. The testing results are shown in Figure 88. The frequency measurement is very stable and accurate. This test demonstrated the high-precision measurement accuracy of the developed algorithm, even using low-cost hardware.

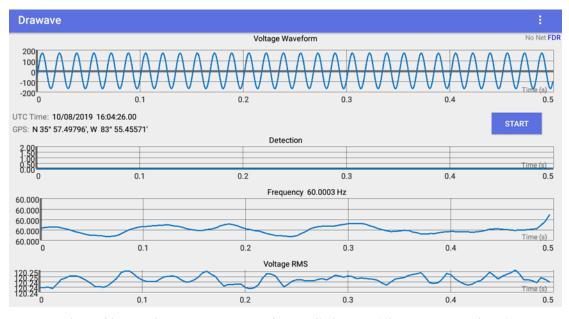


Figure 88 Real-time measurements of PMU Calibrator (60 Hz clean test signal)

# **5.3.2.2** Frequency measurement tests under voltage step change

The experiments utilized the Dip function of the calibrator which created repeated dips in the output voltage waveform according to the settings. In the experiments, the waveform repeat period was set to 0.5

s and in the middle of this period a voltage dip was applied for 0.05s. In the app, the displaying time range was 0.5s and all curves were refreshed every 0.5s.

Experiments were performed on voltage sags with amplitude reduced to 10% to 90% of the normal voltage amplitude. Figure 89, Figure 90, and Figure 91 show the sag to 70%, 50%, and 20% magnitude, respectively. It could be seen that the transient detector detects the voltage step change event, and the frequency estimator also responses to the transient event. Although we can still see the frequency deviations during the transient, the deviation is very small, far away from the frequency triggering threshold (i.e., 57 Hz for low frequency tripping, and 61 Hz for high frequency tripping).

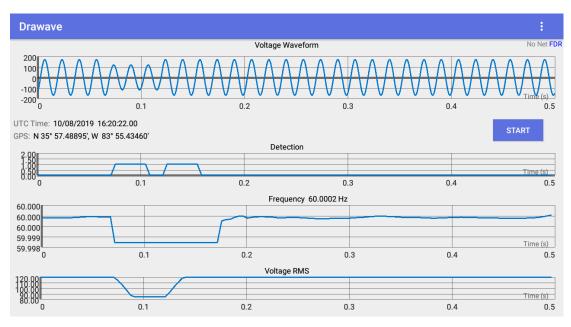


Figure 89 Real-time measurement result under sag to 70% amplitude

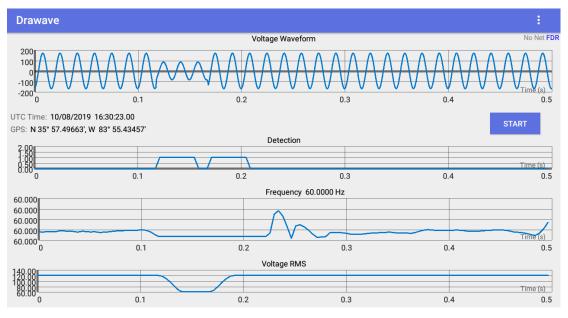


Figure 90 Real-time measurement result under sag to 50% amplitude

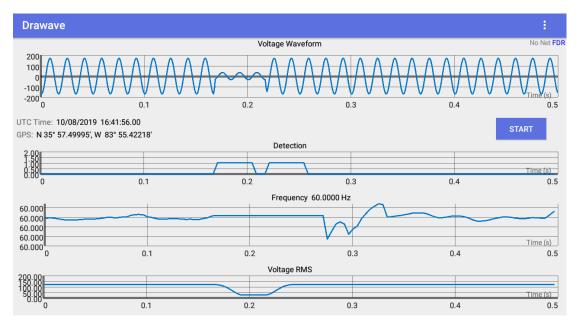


Figure 91 Real-time measurement result under sag to 20% amplitude

### 5.3.2.3 Frequency measurement tests under phase angle jump

To verify whether the developed fault-tolerant algorithm could have good performance during phase jump, experiments were conducted by using the phase jump waveforms with leading  $60^\circ$  and lagging  $60^\circ$ , which were produced with IEEE 118-Bus transmission system. For each phase angle jump, the fault was first triggered at a position near the valley of the sinusoidal waveform, and then following faults were triggered at an interval of quarter cycle period. Totally eight phase angle jump waveforms were used in the experiments. And the results are presented from Figure 92 to Figure 99. Like previous results, the fault could be detected timely to perform frequency lock. After the frequency lock was released, the peaks appeared on the frequency curve indicated the adaptive process which increased window size when the waveform became stable gradually. The first frequency value was calculated in 2.5 cycles after the fault alarm was cleared. Then the estimation window will gradually increase to normal window size, and the frequency measurement error will decrease during this period. It should be noted that frequency spikes after the frequency lock release are also much smaller than the frequency triggering threshold (i.e. 57 Hz for low frequency tripping, and 61 Hz for high frequency tripping). These frequency spikes can be reduced by delaying the frequency measurements using a longer estimation window, but the disadvantage is the longer measurement latency.

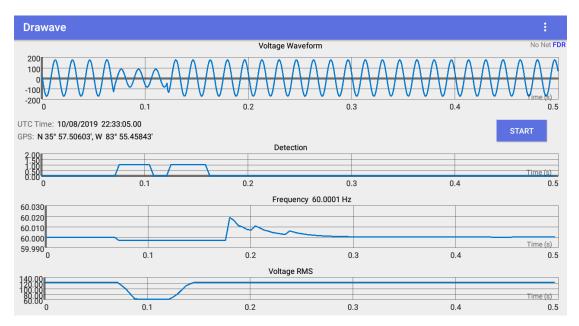


Figure 92 Real-time measurement result under 60-degree phase jump at 0/4 cycle

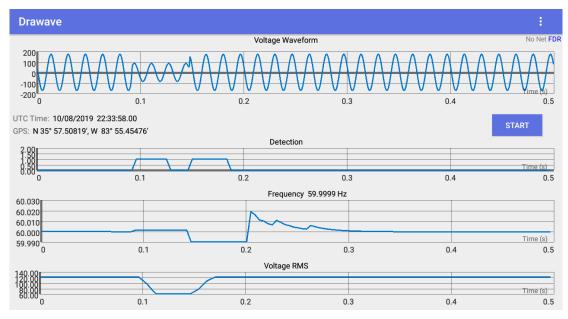


Figure 93 Real-time measurement result under 60-degree phase jump at 1/4 cycle

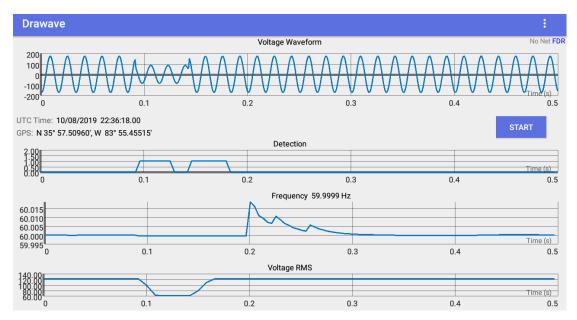


Figure 94 Real-time measurement result under 60-degree phase jump at 2/4 cycle

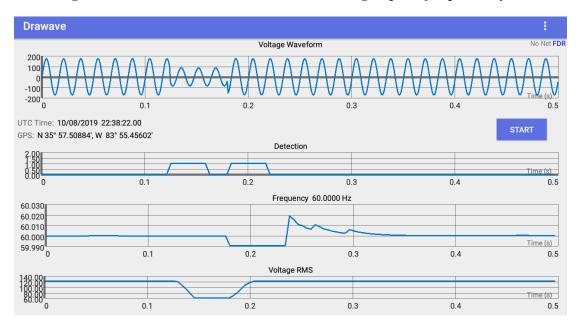


Figure 95 Real-time measurement result under 60-degree phase jump at 3/4 cycle

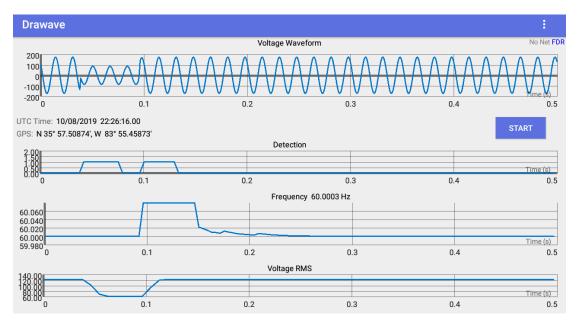


Figure 96 Real-time measurement result under -60-degree phase jump at 0/4 cycle

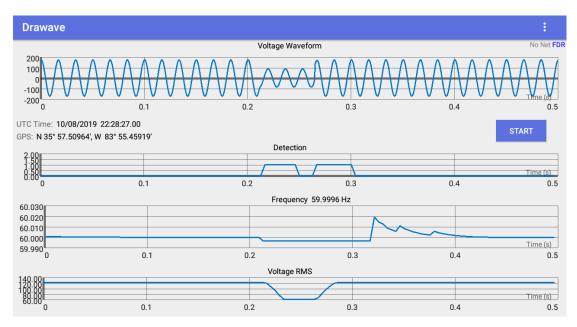


Figure 97 Real-time measurement result under -60-degree phase jump at 1/4 cycle

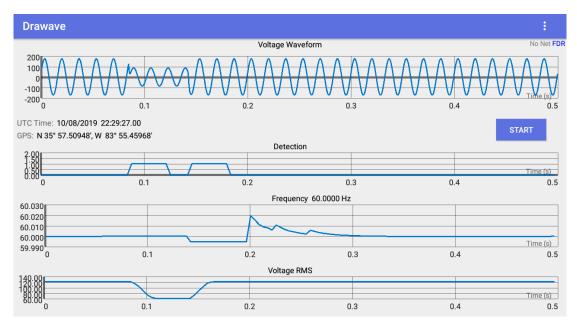


Figure 98 Real-time measurement result under -60-degree phase jump at 2/4 cycle

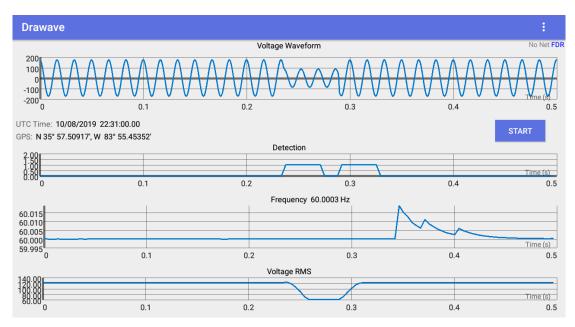


Figure 99 Real-time measurement result under -60-degree phase jump at 3/4 cycle

To summarize, the proposed fault-tolerant frequency measurement algorithm was implemented on low-cost hardware to realize real-time fault detection and frequency measurement. Experiments were conducted to test the real time performance. First, voltage sag with different magnitude from 10% to 90% were used to test the frequency measurement algorithm. Then, voltage waveforms with phase angle jump were used for further tests. The experiment results verified that the proposed fault-tolerant frequency measurement algorithm has good performance in all test cases. Large frequency measurement errors incurred by conventional methods without fault-tolerant mechanism could be significantly reduced. And hence the reliability of frequency measurement was improved, and it could contribute to higher reliability of grid protection for solar power inverters.

### Milestone #4

The work in section 5.2 and section 5.3 completed milestone # 4.

Milestone	Metric Definition	Finished Date
4	Practicality of the algorithm demonstrated by analyzing the computational complexity of the developed algorithm in terms of multiplications and additions, and space requirement, comparing with modern low-cost processor, and demonstration on low-cost FNET/GridEye sensors	09/30/2019

# 5.4 GE INVERTER TESTING IN RTDS

# 5.4.1 Test Case Playback in RTDS

The test case # 1 to 12 performed in section 5.1.4 dependability test has been performed in the RTDS with GE Brilliance 1 MW inverter hardware in loop. The system setup is shown in Figure 100 and the RTDS data playback model is shown in Figure 101. The inverter's response is shown in Figure 102 - Figure 113. In each test case, the terminal's RMS voltage, inverter's current, frequency measured by PLL, and inverter output power are shown. It should be noted that the PLL frequency is measured in the fastest task in the inverter control. It is used for the inverter control including the transient control during the fault ride through and is not used for protection. In GE inverter, a comprehensive and robust protection algorithm is designed for the frequency related protection.

The transient fault with phase angle step change was applied around 0.25 second and lasts for about 3 fundamental frequency cycles. We can see the voltage distortion in first plot and corresponding frequency measurement distortions in the third plot. In these tests, the GE inverter successful rode through without erroneous trips. The reason for this is as mentioned earlier: GE inverter does not do the protection based on the fast PLL frequency measurements but instead uses dedicated logic to provide those protections. Although we can see the large frequency measurement swings during the system transient events, they don't trip the inverter during the transient. On the contrary, this measurement is necessary for the quick identification of the grid transient events and therefore provides corrective control measures to ride through the transients.



Figure 100 GE Brilliance inverter HIL system

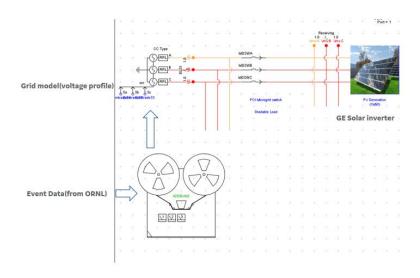


Figure 101 The RTDS model used for the test

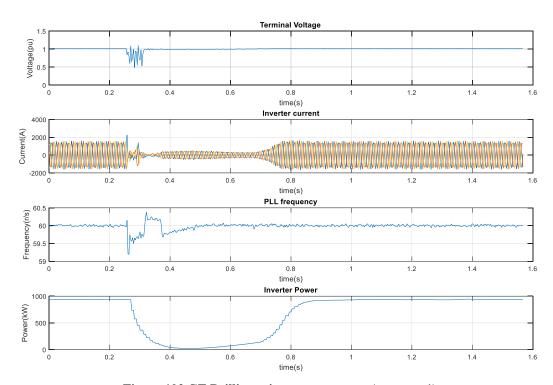


Figure 102 GE Brilliance inverter response (test case 1)

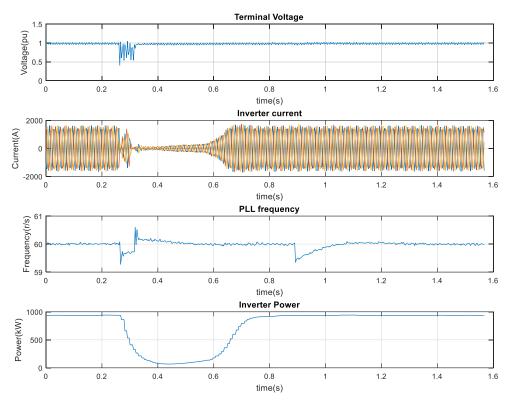


Figure 103 GE Brilliance inverter response (test case 2)

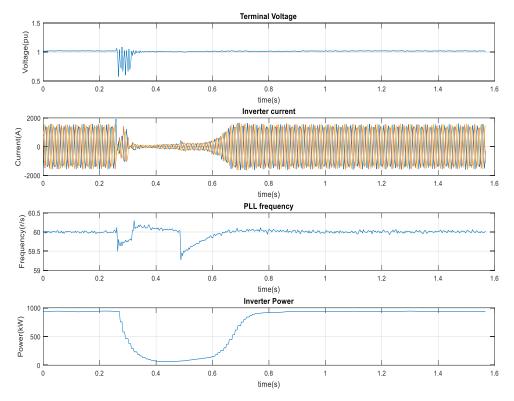


Figure 104 GE Brilliance inverter response (test case 3)

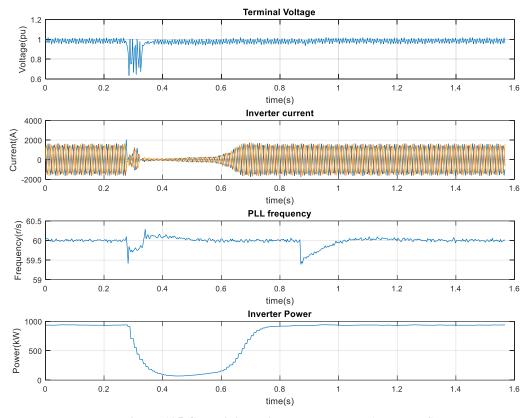


Figure 105 GE Brilliance inverter response (test case 4)

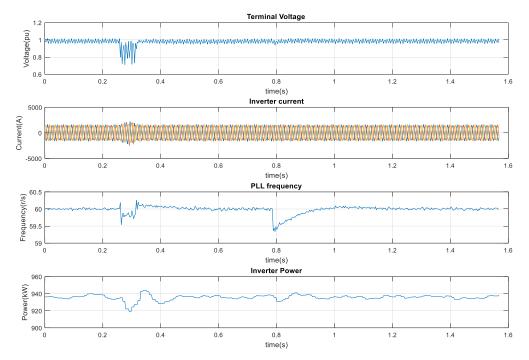


Figure 106 GE Brilliance inverter response (test case 5)

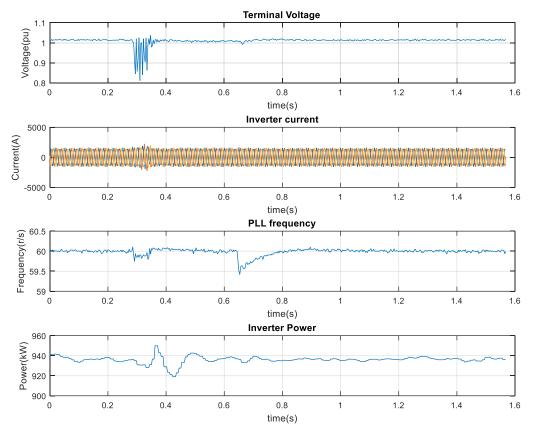


Figure 107 GE Brilliance inverter response (test case 6)

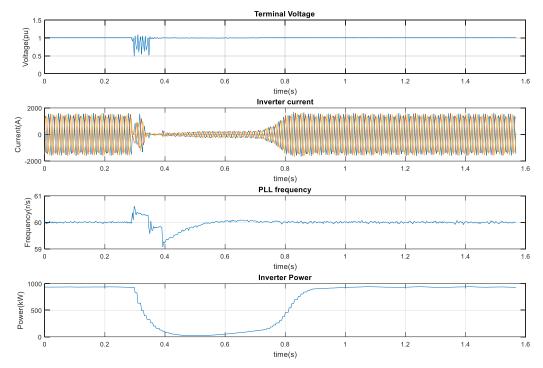


Figure 108 GE Brilliance inverter response (test case 7)

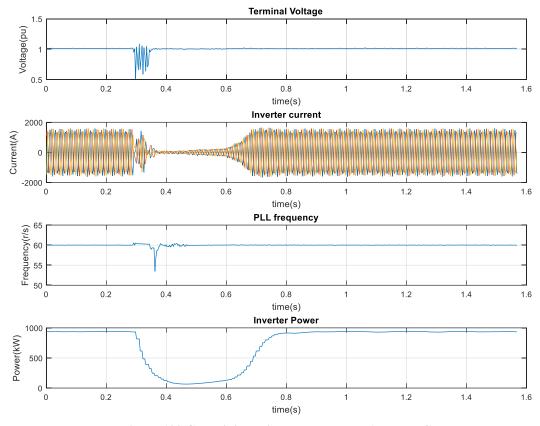


Figure 109 GE Brilliance inverter response (test case 8)

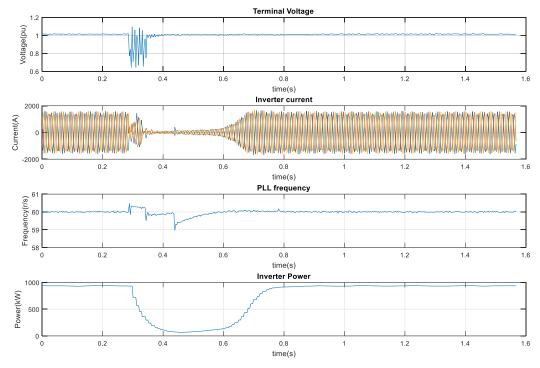


Figure 110 GE Brilliance inverter response (test case 9)

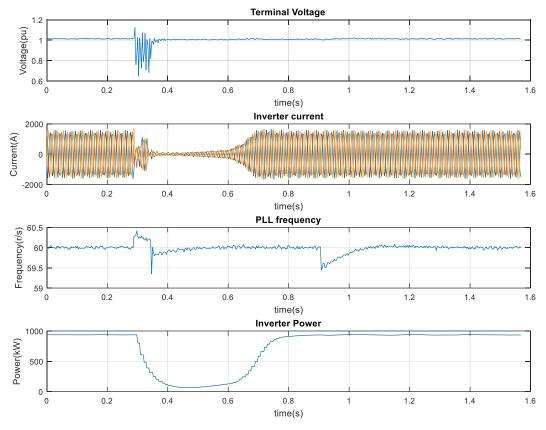


Figure 111 GE Brilliance inverter response (test case 10)

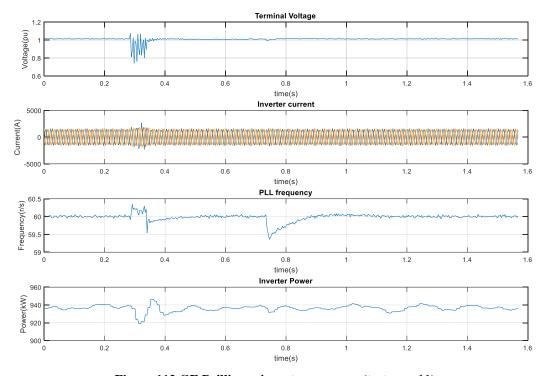


Figure 112 GE Brilliance inverter response (test case 11)

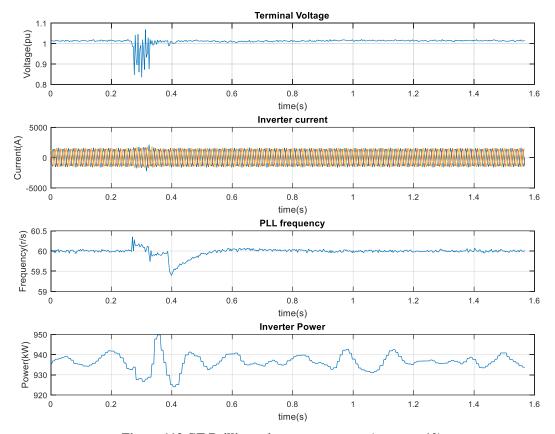


Figure 113 GE Brilliance inverter response (test case 12)

# 5.4.2 IEEE 13 node system test

To further test the response of GE 1 MW commercial solar inverter to grid events, the IEEE 13 Node test feeder is integrated into the RTDS with 1 MW brilliance solar inverter. The IEEE 13 node system is operated at 4.16kV and is widely used to test common features of distribution analysis software. It is characterized by being short, relatively highly loaded, a single voltage regulator at the substation, overhead and underground lines, shunt capacitors, an in-line transformer, and unbalanced loading. An illustration of the system is shown in Figure 114 and Table XVIII highlights the overall characteristics of this system. Also, for the purpose of this study, an additional three-phase capacitor bank is added at Node 680. More details of the system are listed in Appendix B.

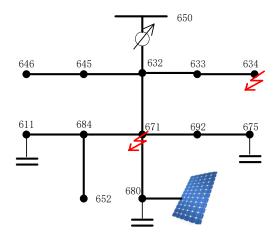


Figure 114 IEEE 13 system model used for the test and the PV connection

Table XVIII IEEE 13 Node System overview

	IEEE 13
Volt Level	4.16 kV
No. of Nodes	13
No. of Line Sections	12
Total Conductor Length	8200 ft (2.5 km)
Total Load	3.47 MW
No. of Regulators	1
No. of Capacitor Banks	1 (3-Ph) + 1 (1-Ph)

As shown in Figure 114, the GE 1 MW solar inverter was integrated into the grid through Node 680. A model overview in RTDS is shown in Figure 115.

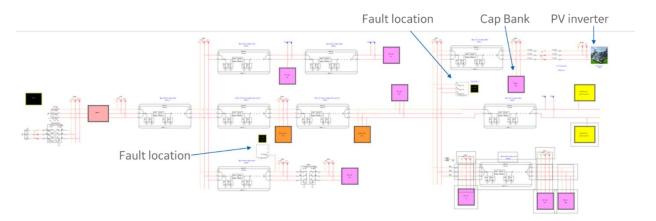


Figure 115 Test system in RTDS

# **5.4.2.1** Test scenarios

To test the performance of GE inverter, especially the frequency related protection, the following test scenarios are defined:

- 2 fault locations: Node 671 and Node 634
- 3 fault types: single line to ground (SLG), line to line to ground (LLG), and three-phase fault (3-ph)
- 2 fault durations: 5 cycles and 60 cycles
- 2 capacitor switching events: single phase capacitor and three-phase capacitor

The focus of the tests is the frequency measurements inside the inverter and how the inverter is responding to the events. For comparison, both the PLL frequency and grid monitor frequency were captured during the tests. PLL frequency is used by the control and grid monitor frequency is used for the protection purpose or other grid compliance related algorithms.

For better reference of the test results in the following section, the following cases are defined.

- Case 1: A LLG fault event at Node 671 with fault duration of 5 cycles
- Case 2: A LLG fault event at Node 671 with fault duration of 60 cycles
- Case 3: A 3-Ph fault event at Node 671 with fault duration of 5 cycles
- Case 4: A 3-Ph fault event at Node 671 with fault duration of 60 cycles
- Case 5: A SLG fault event at Node 634 with fault duration of 5 cycles
- Case 6: A SLG fault event at Node 634 with fault duration of 60 cycles
- Case 7: 1-Ph capacitor switch on at Node 680
- Case 8: 3-Ph capacitor switch on at Node 680

# 5.4.2.2 Test results

Both the inverter internal recordings and the RTDS runtime recordings were captured in the tests. The inverter side waveforms were captured using high speed capture buffer (depending on the total duration of data capture, different sampling rate might be used) to provide a better view of inverter internal variables during the transient. The RTDS runtime recordings are only for the accessible measurement points. The following signals are captured at the inverter side: inverter terminal voltage magnitude, PLL frequency, grid monitor frequency and inverter power; at the RTDS side, the grid voltages and currents are captured.

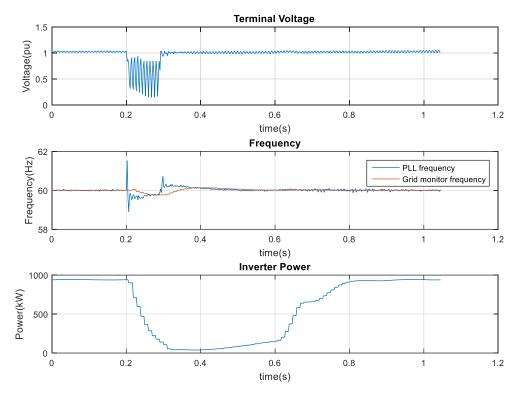


Figure 116 Inverter measurements for case1

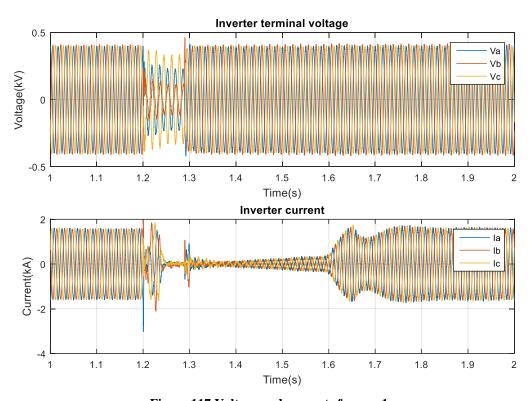


Figure 117 Voltage and currents for case1

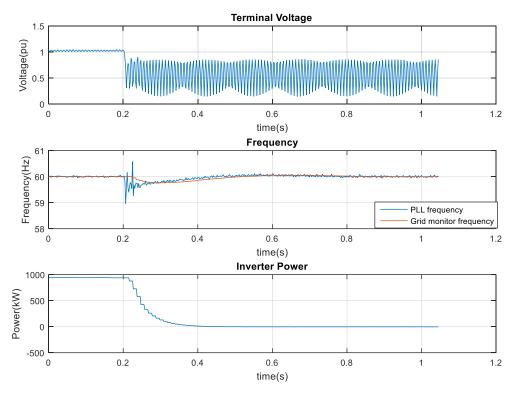


Figure 118 Inverter measurements for case2

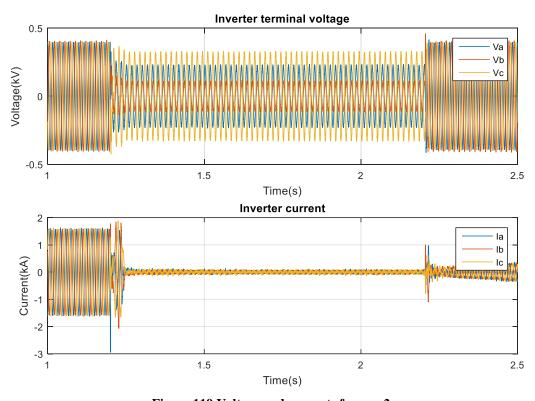


Figure 119 Voltage and currents for case2

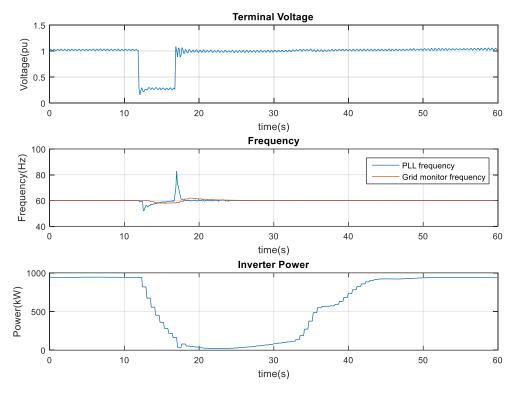


Figure 120 Inverter measurements for case3

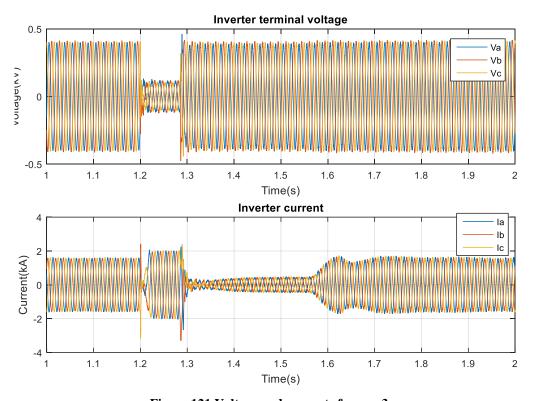


Figure 121 Voltage and currents for case3

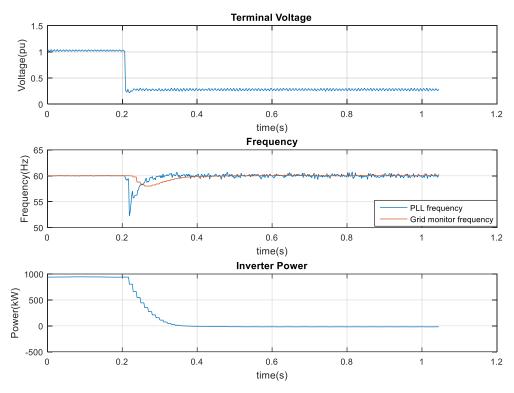


Figure 122 Inverter measurements for case4

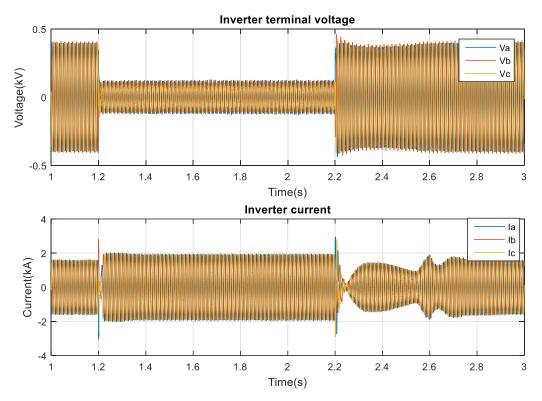


Figure 123 Voltage and currents for case4

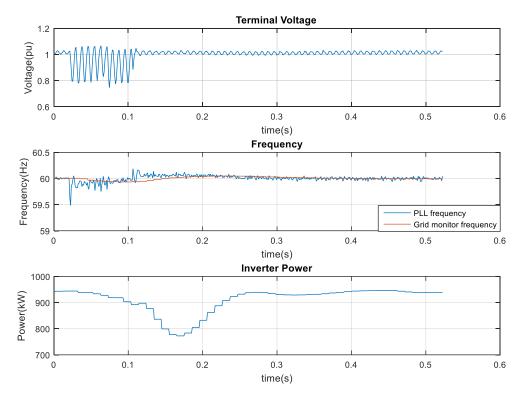


Figure 124 Inverter measurements for case5

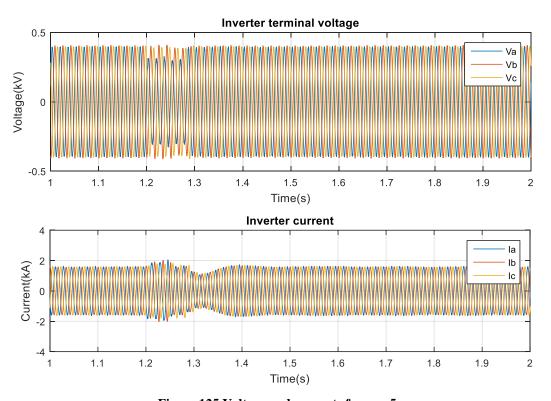


Figure 125 Voltage and currents for case5

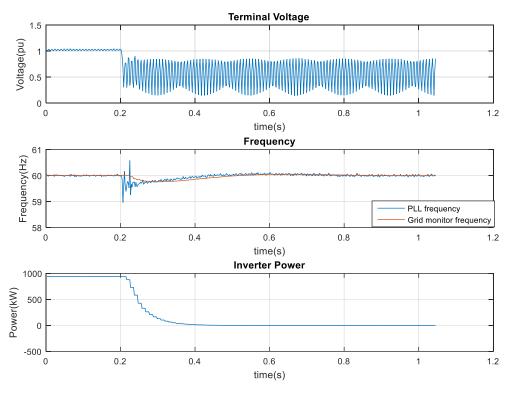


Figure 126 Inverter measurements for case6

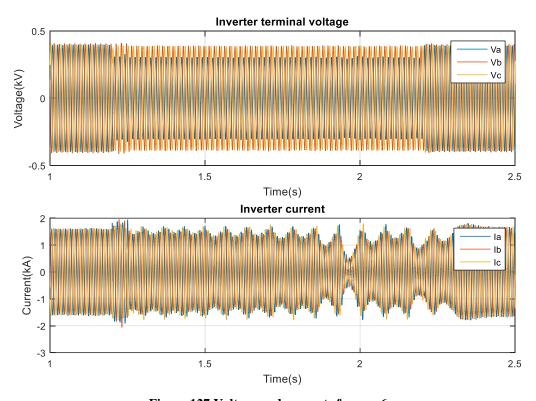


Figure 127 Voltage and currents for case6

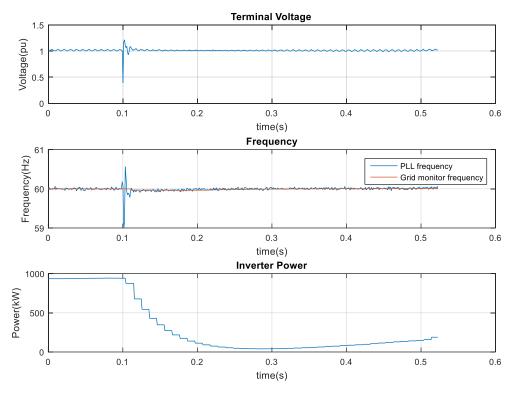


Figure 128 Inverter measurements for case7

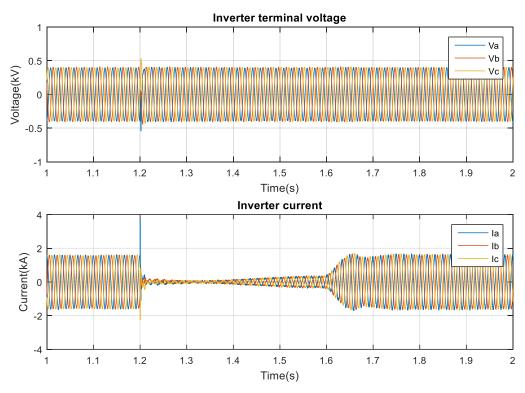


Figure 129 Voltage and currents for case7

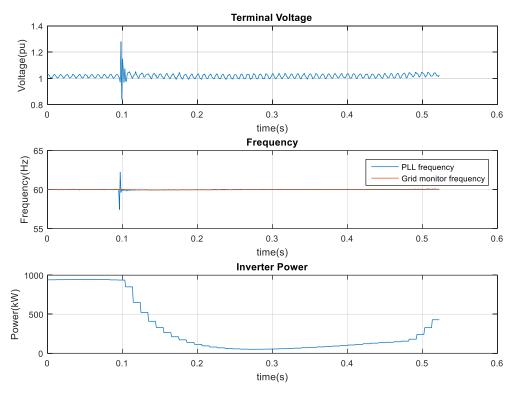


Figure 130 Inverter measurements for case8

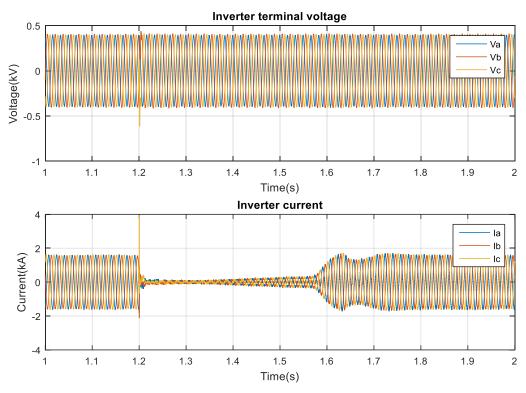


Figure 131 Voltage and currents for case8

For all the cases, we can see from the results above that the faults cause sudden changes in the voltage phase angle and voltage magnitude which can be picked up correctly by the inverter PLL and the inverter

can ride through the events successfully. Also we noticed that the frequency measurements inside the inverter don't trigger the inverter protection.

# 5.4.3 Summary

The inverter was tested in the IEEE 13 bus system. Different fault scenarios were created to test the inverter performance. The observation is basically the same with the previous tests using playback of generated grid events data.

Under all events, GE inverter does not trip on erroneous frequency measurements. Although the inverter PLL does have transient frequency measurement, it is necessary for the inverter to synchronize with the grid. The inverter does not directly use the PLL outputs to do the protection. Although not tripping, the GE inverter does have a voltage-dependent power reduction and a ramp feature after the grid recovery which was required by the grid code when the control was developed.

### 6. SIGNIFICANT ACCOMPLISHMENTS AND CONCLUSIONS

Overall, this project, for the first time, proposes a two-step approach to achieve reliable grid measurements. Smooth integration of utility-scale PV is critical to SETO's 2030 target. The 700 MW utility-scale PV trip event revealed the vulnerability of PV inverters to the inaccurate frequency measurement, and the existing corrective action cannot fully solve the problem. By developing a fault-tolerant frequency algorithm, this project provides a fundamental solution to mitigate the reliability risk caused by incorrect frequency measurement.

To be specific, the state-of-the-art and commercially wide used measurement technologies have been reviewed first, and the frequency measurement problem of these technologies during the system transient faults has been investigated and tested. The tests verified that they fail to provide reliable measurements during the system transients, like what happened during the Blue Cut Fire event. The tests discovered that the existing technologies failed because they assume the power grid waveforms are always continuous in the sinusoidal format. These assumptions are true when the power system operates normally without a transient fault. However, during a system transient fault, the grid waveforms are usually highly distorted and could have discontinuity due to phase angle jump (or magnitude step change), and unfortunately the transient period cannot be modeled or monitored by these measurement technologies, which results in large measurement errors. Based on the analysis, this project has developed a fault-tolerant frequency measurement technology that can overcome this problem.

First, the team has developed a grid transient detector for the fault-tolerant frequency measurement algorithm, which state-of-the-art measurement technologies does not have. The detector can detect the sudden change of grid signals like phase-jump by using the point-on-wave based approach. Different levels of phase-jump have been used to test the detector. In the meantime, the detector can ride through the signal distortions that will not cause significant measurement errors such as noise and harmonic distortions. High level of noise and harmonics have been tested to verify the performance of the detector. In brief, by detecting grid signals' transients and providing the detection information to the following frequency estimator, the detector is a key component to address the measurement uncertainties under system transients.

Second, based on the developed grid transient detector, the team has developed an intelligent fault-tolerant frequency estimator, which is adaptive to the grid transient events. The factors that impact the fault threshold have been studied, and the method to determine the threshold has been presented. It has been found that the threshold value is nearly proportional to the sampling frequency. Signals of different frequency with different levels of noise, harmonics, voltage sag, and phase-jump have been used to test the proposed fault-tolerant frequency estimator. The frequency measurement errors are within 0.5 Hz in two cycles and 0.05 Hz in 6 cycles following a phase-jump event, and fully meet milestone 2. Overall, during the system transient events, the whole estimator goes through four stages from frequency lock, extreme fast frequency estimation, fast frequency estimation, and classical frequency estimation. This multiple stage approach has two advantages over classical approach: 1) mitigate frequency estimation errors during system transients; 2) provide frequency measurements in a timely manner after faults by using the extreme fast and fast frequency estimator. Additionally, this approach also maintains the high-level measurement accuracies as this approach will recover to a classical algorithm when the system transients end.

Further, the team has performed the algorithm evaluation from two aspects: dependability and security. The two tests demonstrated the algorithm can detect system transient faults and mitigate measurement errors, and do not mistakenly response to the system distortions that will not cause large measurement errors. In addition, the tests have been also performed in RTDS by GE Global Research. The two tests

will give user confidence that the algorithm can provide reliable measurements during system transient faults, which cannot be provided by state-of-the-art algorithms.

Then, the team has analyzed the computational complexity of the new algorithm and performed computation time test on the new algorithm. The analysis and test verified the low computation cost of the algorithm, which is critical for widely use. Moreover, a low-cost measurement instrument was developed, and the new algorithm was implemented on the instrument. The instrument can run in real-time and provide fault-tolerant measurements. The successful implementation of the new algorithm on low-cost hardware further demonstrated the low computation cost of the new algorithm.

The developed fault-tolerant measurement technology has been tested thoroughly under different steady-state, dynamic, and transient conditions to verify its measurement accuracy, security, and dependability, thus demonstrating the measurements provided by the technology are trustworthy. Further, a real-time fault-tolerant monitor has been developed, built and tested using the highly accurate 6135A/PMUCAL Phasor Measurement Unit Calibration System. The hardware test justified the measurement performance. More importantly, the hardware is a low-cost platform, and it verified the developed method has low computational complexity, which is critical for commercial product adoption.

# 7. INVENTIONS, PATENTS, PUBLICATIONS, AND OTHER RESULTS

### 7.1 INVENTIONS

Lingwei Zhan, et al., "A fault-tolerant grid frequency estimation method during transients," Invention disclosure.

# 7.2 PATENTS

Lingwei Zhan, et al. "Fault-Tolerant Grid Frequency Measurement Algorithm During Transients", USSN, 17/204,494.

### 7.3 PUBLICATIONS

Zhan, Lingwei; Xiao, Bailu; Li, Fuhua; Yin, He; Yao, Wenxuan; Li, Zhi; Liu, Yilu: 'Fault-tolerant grid frequency measurement algorithm during transients', IET Energy Systems Integration, 2020, 2, (3), p. 173-178, DOI: 10.1049/iet-esi.2019.0126.

Fuhua Li, et al., Frequency Computation During Events and Point-on-wave Measurement, 2019 IEEE Power & Energy Society General Meeting, Atlanta, GA, 2019, pp. 1-5.

J. Zhao, L. Zhan, et al., "Recent Development of Frequency Estimation Methods for Future Smart Grid," in IEEE Open Access Journal of Power and Energy, vol. 7, pp. 354-365, 2020, doi: 10.1109/OAJPE.2020.3029984.

#### 7.4 PRESENTATIONS

Lingwei Zhan, "Development of low-cost time synchronized point-on-wave data recorder and fault-tolerant grid frequency measurements technologies," NASPI, April 15-17, 2019, San Diego, CA.

Lingwei Zhan, "Towards High Speed and Fault-tolerant Grid Measurements", WECC JSIS Meeting, November 14-15, Salt Lake City, Utah.

Fuhua Li, "Frequency Computation During Events and Point-on-wave Measurement", 2019 IEEE Power & Energy Society General Meeting, Atlanta, GA, 2019, pp. 1-5.

#### 8. PATH FORWARD

Field Data Validation and Demonstration: Field validation and demonstration is one step forward towards commercialization. First, we can collaborate with utilities by testing the new measurement technology with the Digital Fault Recorder (DFR) data, which can provide different types of real system transient faults data. By utilizing large amount of DFR data, the measurement performance of the developed technology can be thoroughly evaluated, and its limitations could also be revealed, which is very useful for further improvement. In the meantime, the validation test may also help discover any other unknown measurement challenge. Further, the developed fault-tolerant monitor can be deployed in field to continuously collect grid data and monitor the power grid transients.

DER Hardware Demonstration: A critical work towards widely industry adoption is to demonstrate the developed technology on DER system like the solar inverters and perform Hardware in Loop (HIL) testing. As this project is a one-year project and the problem being solved is new, existing hardware test platform, instead of commercial GE's solar inverters, was used in the project period to evaluate the new algorithm. The existing hardware platform has a PMU calibrator that can generate various and accurate test signals, which are critical for performance evaluation. Next step of this project is to develop the algorithm in solar inverters, some companies including GE have shown interests in developing the new algorithm in their products. It should be noted that this work need careful evaluation since measurements are fundamental low-level component for high-level grid control and protection applications, and the new measurement method shall not negatively impact the applications. A good strategy is to implement the new method in parallel with the existing measurement method and two methods perform the measurement in parallel.

Real-time Grid Anomaly Alarming: In fact, the real-time "transient detector" developed in this project can operate separately from the frequency estimator. In other words, the transient detector can operate independently to monitor the occurrence of "transient fault" in real-time. Therefore, it can be applied to monitor grid anomalies, and any other measurements, not limited to frequency measurement, and data analysis applications can use the transient detector data for secure and reliable grid operations.

### 9. REFERENCE

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# APPENDIX A. DEPENDABILITY TEST

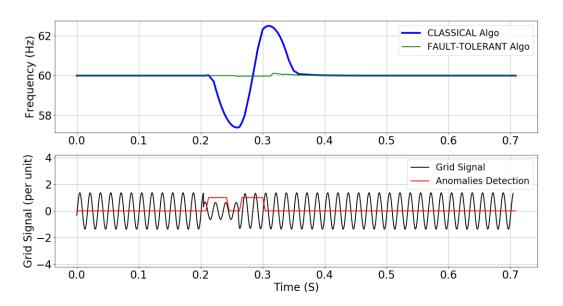


Figure 132 time domain result – case 2

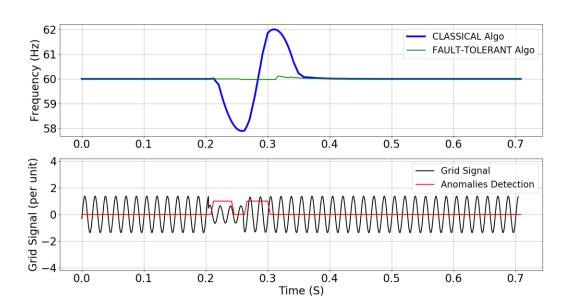


Figure 133 time domain result – case 3

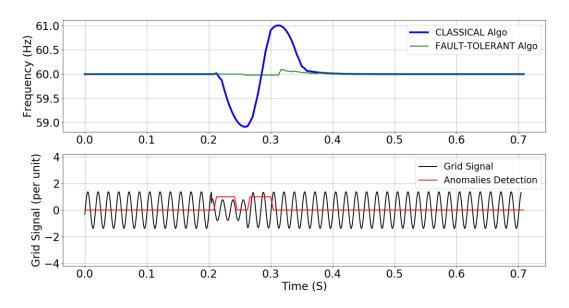


Figure 134 time domain result - case 4

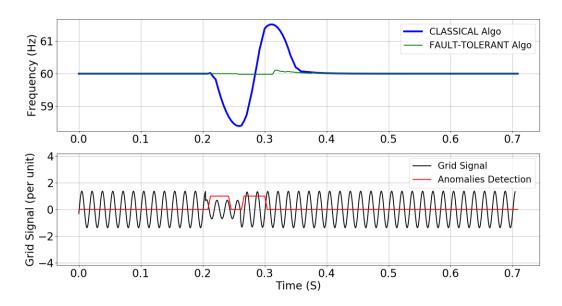


Figure 135 time domain result – case 5

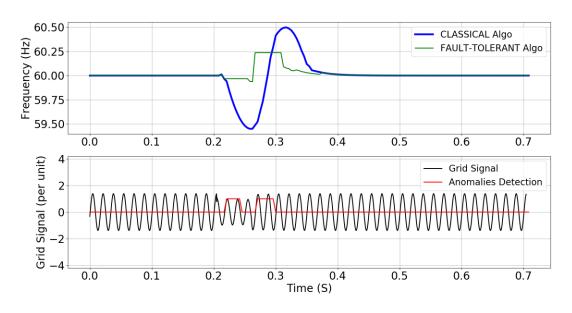


Figure 136 time domain result – case 6

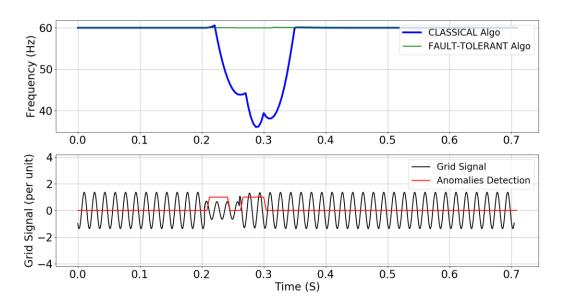


Figure 137 time domain result – case 7

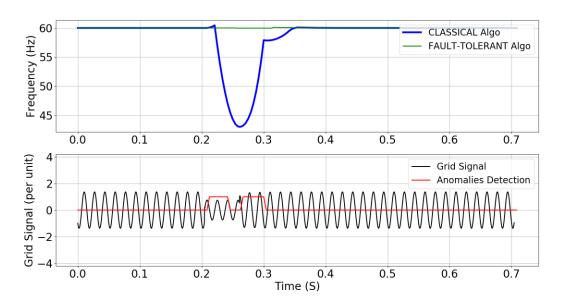


Figure 138 time domain result – case 8

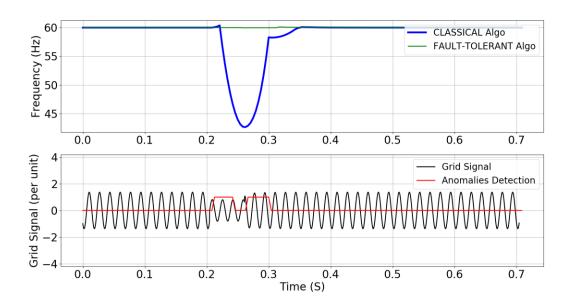


Figure 139 time domain result – case 9

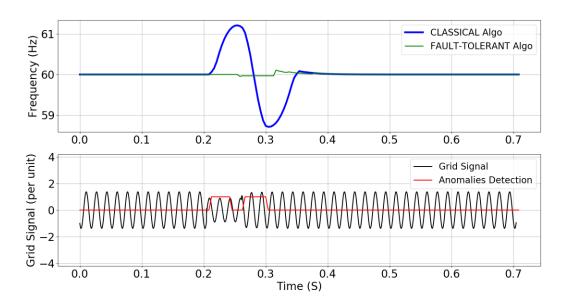


Figure 140 time domain result – case 10

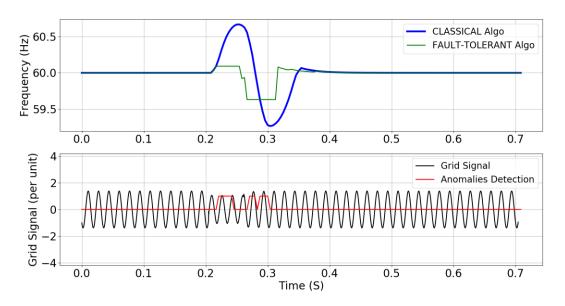


Figure 141 time domain result – case 11

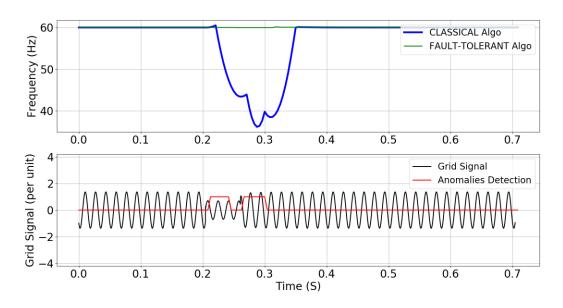


Figure 142 time domain result – case 12

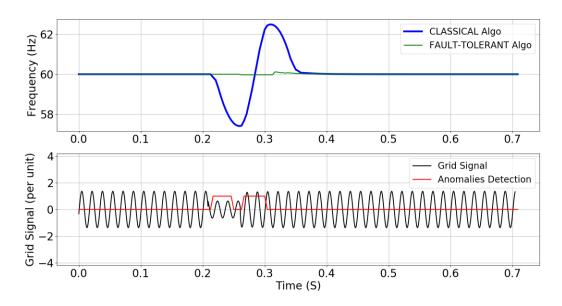


Figure 143 time domain result – case 14

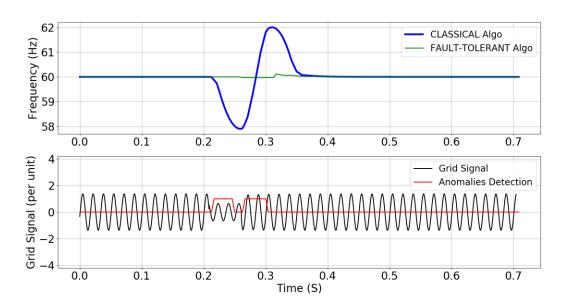


Figure 144 time domain result – case 15

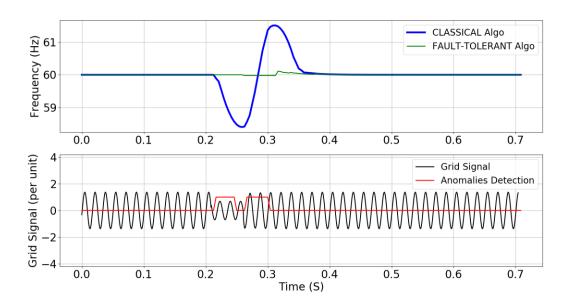


Figure 145 time domain result – case 16

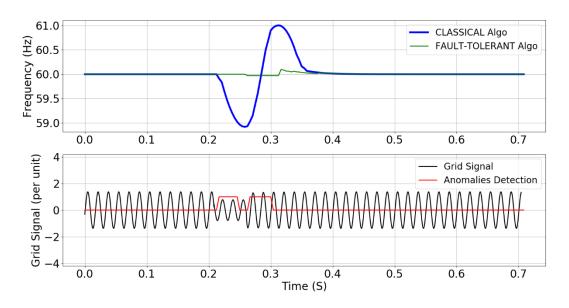


Figure 146 time domain result – case 17

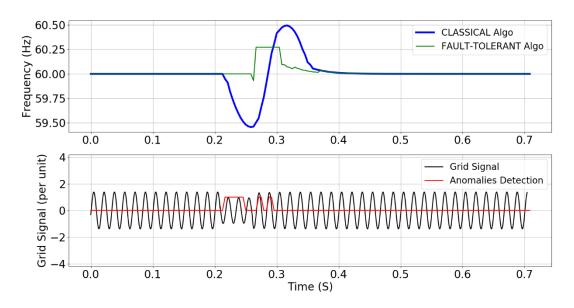


Figure 147 time domain result – case 18

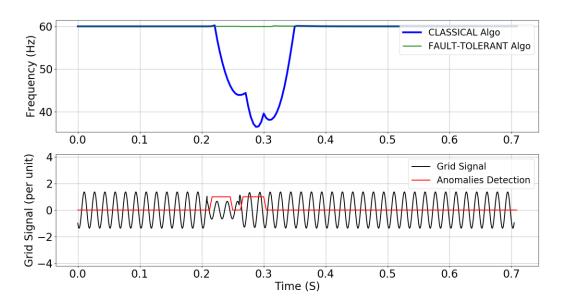


Figure 148 time domain result - case 19

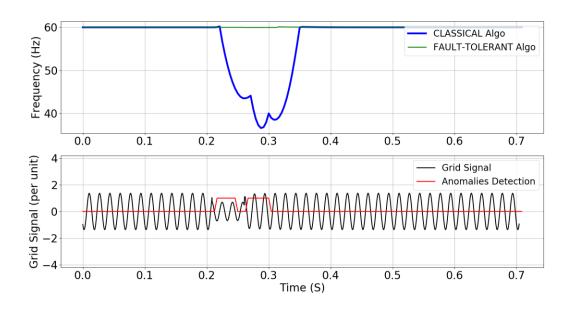


Figure 149 time domain result – case 20

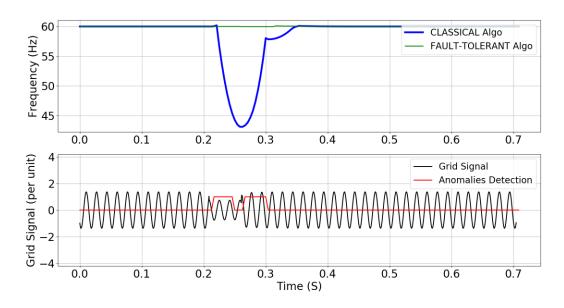


Figure 150 time domain result – case 21

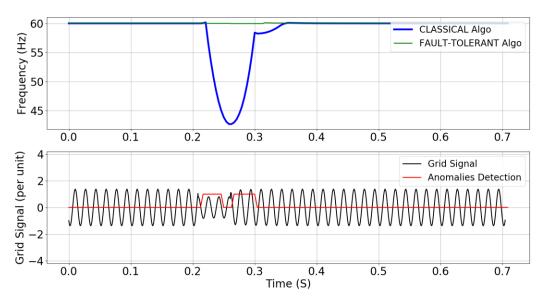


Figure 151 time domain result – case 22

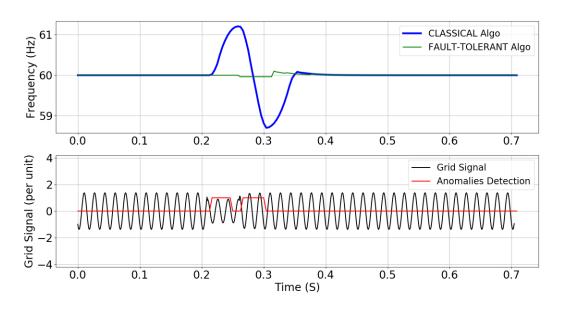


Figure 152 time domain result – case 23

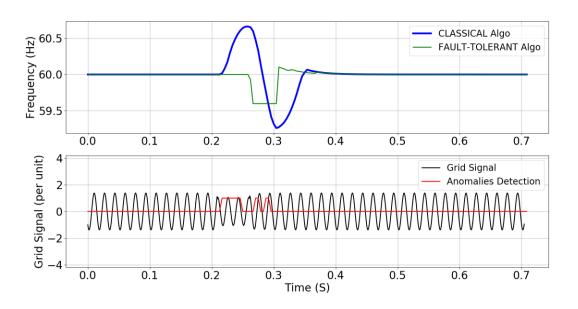


Figure 153 time domain result – case 24

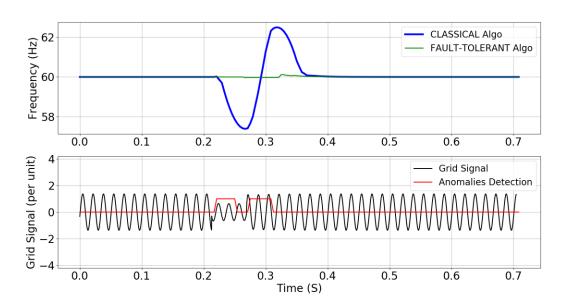


Figure 154 time domain result – case 26

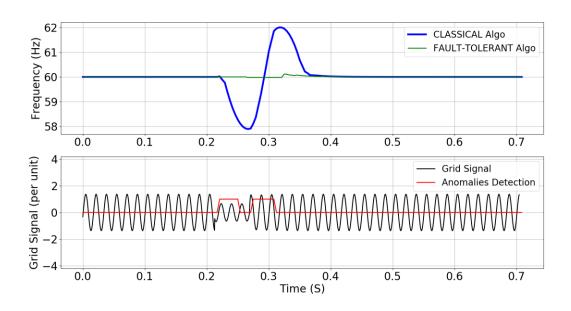


Figure 155 time domain result – case 27

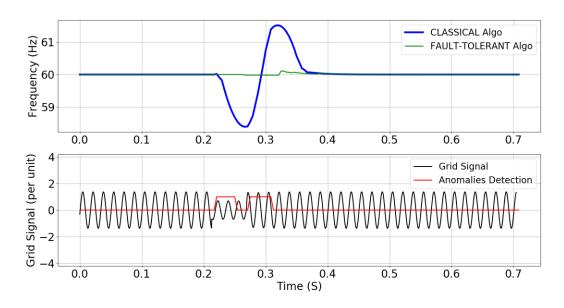


Figure 156 time domain result – case 28

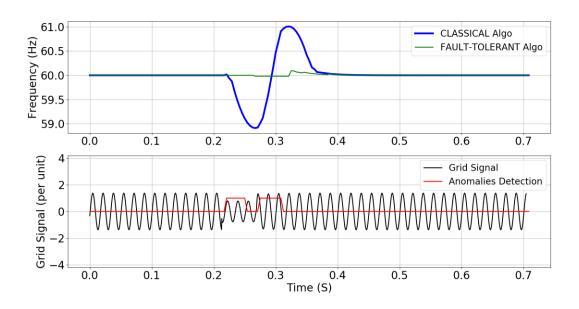


Figure 157 time domain result - case 29

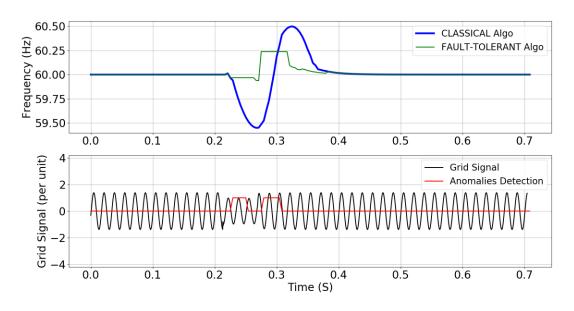


Figure 158 time domain result – case 30

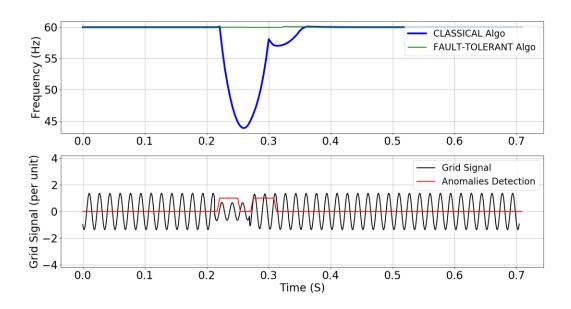


Figure 159 time domain result – case 31

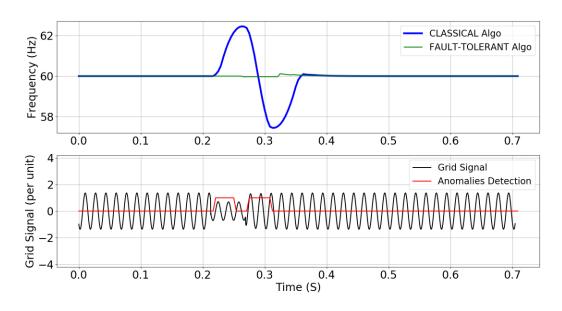


Figure 160 time domain result – case 32

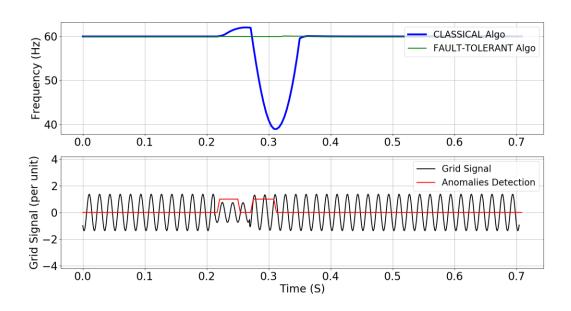


Figure 161 time domain result – case 33

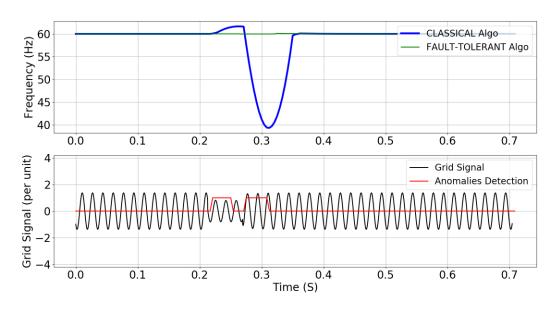


Figure 162 time domain result - case 34

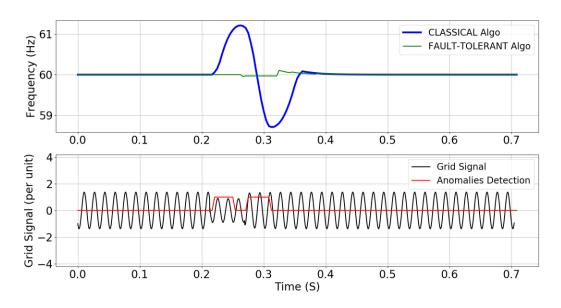


Figure 163 time domain result – case 35

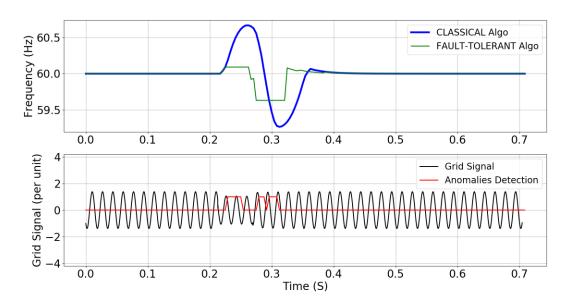


Figure 164 time domain result – case 36

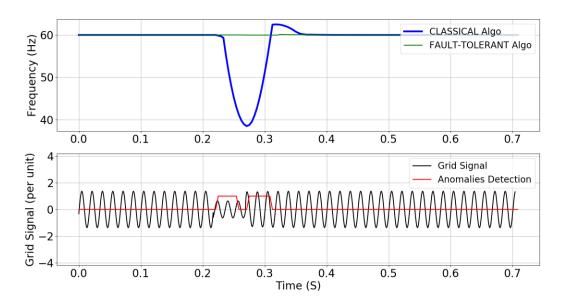


Figure 165 time domain result – case 38

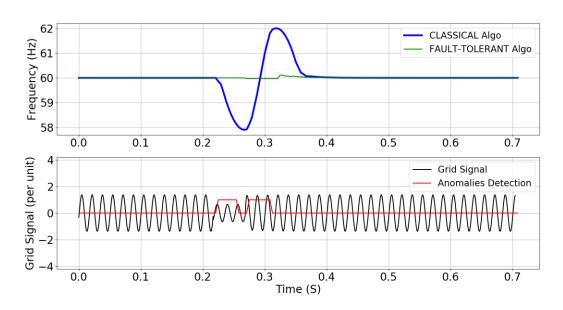


Figure 166 time domain result - case 39

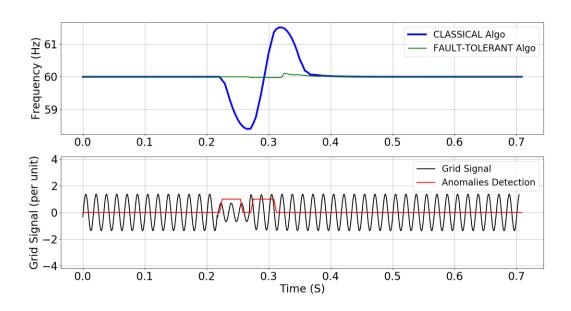


Figure 167 time domain result – case 40

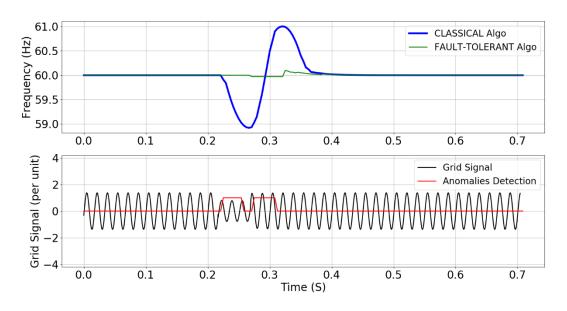


Figure 168 time domain result – case 41

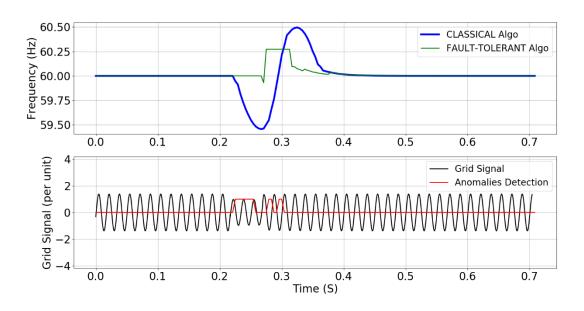


Figure 169 time domain result – case 42

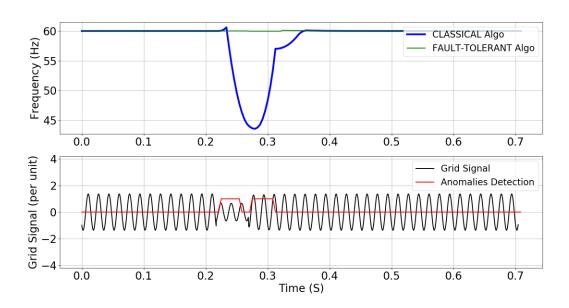


Figure 170 time domain result – case 43

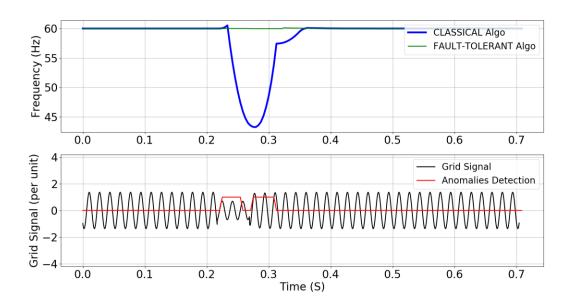


Figure 171 time domain result – case 44

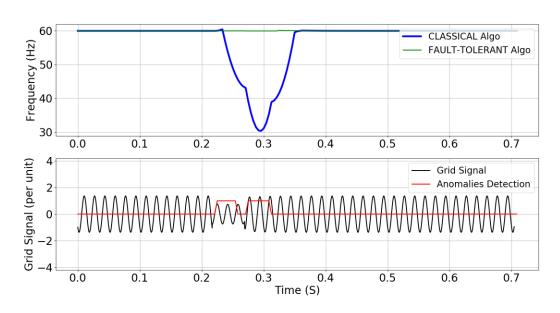


Figure 172 time domain result – case 45

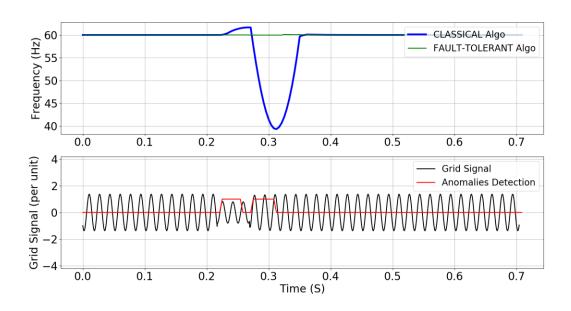


Figure 173 time domain result – case 46

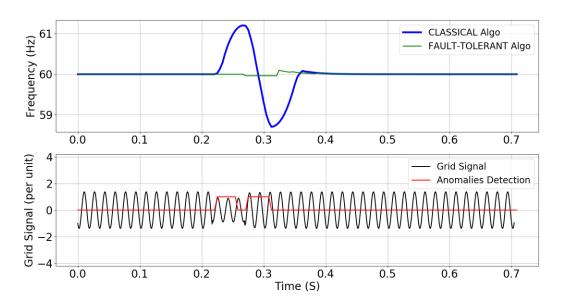


Figure 174 time domain result – case 47

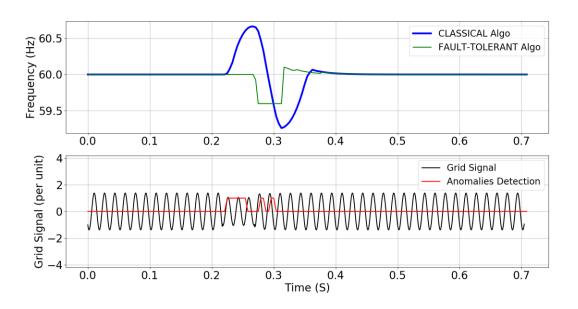


Figure 175 time domain result – case 48

### APPENDIX B. IEEE 13 FEEDER NODE SYSTEM

# Overhead Line Configuration Data:

Config.	Phasing	Phase	Neutral	Spacing
		ACSR	ACSR	ID
601	BACN	556,500 26/7	4/0 6/1	500
602	CABN	4/0 6/1	4/0 6/1	500
603	CBN	1/0	1/0	505
604	ACN	1/0	1/0	505
605	CN	1/0	1/0	510

# Underground Line Configuration Data:

Config.	Phasing	Cable	Neutral	Space ID
606	ABCN	250,000 AA, CN	None	515
607	A N	1/0 AA, TS	1/0 Cu	520

# Line Segment Data:

Node A	Node B	Length(ft.)	Config.
632	645	500	603
632	633	500	602
633	634	0	XFM-1
645	646	300	603
650	632	2000	601
684	652	800	607
632	671	2000	601
671	684	300	604
671	680	1000	601
671	692	0	Switch
684	611	300	605
692	675	500	606

### Transformer Data:

	kVA	kV-high	kV-low	R - %	X - %
Substation:	5,000	115 - D	4.16 Gr. Y	1	8
XFM -1	500	4.16 – Gr.W	0.48 – Gr.W	1.1	2

# Capacitor Data:

Node	Ph-A	Ph-B	Ph-C
	kVAr	kVAr	kVAr
675	200	200	200
611			100
680	200	200	200

### Regulator Data:

Regulator ID: 1
-----------------

Line Segment:	650 - 632		
Location:	50		
Phases:	A - B -C		
Connection:	3-Ph,LG		
Monitoring Phase:	A-B-C		
Bandwidth:	2.0 volts		
PT Ratio:	20		
Primary CT Rating:	700		
Compensator Settings:	Ph-A	Ph-B	Ph-C
R - Setting:	3	3	3
X - Setting:	9	9	9
Voltage Level:	122	122	122

# Spot Load Data:

Node	Load	Ph-1	Ph-1	Ph-2	Ph-2	Ph-3	Ph-3
	Model	kW	kVAr	kW	kVAr	kW	kVAr
634	Y-PQ	160	110	120	90	120	90
645	Y-PQ	0	0	170	125	0	0
646	D-Z	0	0	230	132	0	0
652	Y-Z	128	86	0	0	0	0
671	D-PQ	385	220	385	220	385	220
675	Y-PQ	485	190	68	60	290	212
692	D-I	0	0	0	0	170	151
611	Y-I	0	0	0	0	170	80
	TOTAL	1158	606	973	627	1135	753

### Distributed Load Data:

Node A	Node B	Load	Ph-1	Ph-1	Ph-2	Ph-2	Ph-3	Ph-3
		Model	kW	kVAr	kW	kVAr	kW	kVAr
632	671	Y-PQ	17	10	66	38	117	68